SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

PRODUCT SETUP

SECURITY

Command Password Access Timeout	5 min
Setting Password Access Timeout	30 min
Invalid Password Attempts	3
Password Lockout Duration	5 min
Password Access Events	Disabled
Local Setting Authorized	ON
Remote Setting Authorized	ON
Access Authorized Timeout	30 min

DISPLAY PROPERTIES

Flash Message Time	1.0 s
Default Message Timeout	300 s
Default Message Intensity (VFD Only)	25 %
Screen Saver Feature (LCD Only)	Disabled
Screen Saver Wait Time (LCD Only)	30 min
Current Cutoff Level	0.020 pu
Voltage Cutoff Level	1.0 V

COMMUNICATIONS

SERIAL PORTS

COM2 Selection	RS485
RS485 Com2 Baud Rate	9600
RS485 Com2 Parity	None
RS485 Com2 Response Min Time	0 ms
RS232 Baud Rate	115200

NETWORK

NETWORK PORT 1

IP Address	10. 1. 21. 49
IP Subnet Mask	255.255.255. 0
Port 1 Function	Enabled

NETWORK PORT 2

IP Address	127. 0. 0. 1
IP Subnet Mask	255. 0. 0. 0
Redundancy	Failover
Port 2 Function	Enabled

NETWORK PORT 3

IP Address	127. 0. 0. 1
IP Subnet Mask	255. 0. 0. 0
Port 3 Function	Enabled

ROUTING

Default Gateway	10. 1. 21. 1
Static Network Route 1: Destination	127. 0. 0. 1
Static Network Route 1: Netmask	255. 0. 0. 0
Static Network Route 1: Gateway	127. 0. 0. 1
Static Network Route 2: Destination	127. 0. 0. 1
Static Network Route 2: Netmask	255. 0. 0. 0
Static Network Route 2: Gateway	127. 0. 0. 1
Static Network Route 3: Destination	127. 0. 0. 1
Static Network Route 3: Netmask	255. 0. 0. 0
Static Network Route 3: Gateway	127. 0. 0. 1
Static Network Route 4: Destination	127. 0. 0. 1
Static Network Route 4: Netmask	255. 0. 0. 0
Static Network Route 4: Gateway	127. 0. 0. 1
Static Network Route 5: Destination	127. 0. 0. 1
Static Network Route 5: Netmask	255. 0. 0. 0
Static Network Route 5: Gateway	127. 0. 0. 1
Static Network Route 6: Destination	127. 0. 0. 1
Static Network Route 6: Netmask	255. 0. 0. 0
Static Network Route 6: Gateway	127. 0. 0. 1

MODBUS PROTOCOL

Modbus Slave Address	254
Modbus TCP Port Number	502

PROTOCOL

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PROTOCOL (continued from last page)

Protocol	DNP 3.0
MMS TCP Connection Timeout	120 s
IEC61850 Edition	Edition1

DNP PROTOCOL

DNP Channel 1 Port	NETWORK - TCP
DNP Channel 2 Port	NONE
DNP Address	8
DNP Client Address 1	10. 1. 21. 45
DNP Client Address 2	0. 0. 0. 0
DNP Client Address 3	0. 0. 0. 0
DNP Client Address 4	0. 0. 0. 0
DNP Client Address 5	0. 0. 0. 0
DNP TCP/UDP Port Number	21000
DNP Unsol Resp Function	Disabled
DNP Unsol Resp Timeout	5 s
DNP Unsol Resp Max Retries	10
DNP Unsol Resp Dest Addr	4
DNP Current Scale Factor	1
DNP Voltage Scale Factor	1
DNP Power Scale Factor	1
DNP Energy Scale Factor	1
DNP Power Factor Scale Factor	1
DNP Other Scale Factor	1
DNP Current Default Deadband	1
DNP Voltage Default Deadband	1
DNP Power Default Deadband	1
DNP Energy Default Deadband	1
DNP Power Factor Default Deadband	1
DNP Other Default Deadband	1
DNP Time Sync IIN Period	1440 min
DNP Message Fragment Size	240
DNP Number of Paired Controls	0
DNP TCP Connection Timeout	120 s
DNP Event Time Base	LOCAL
DNP Object 1 Default Variation	2
DNP Object 2 Default Variation	2
DNP Object 30 Default Variation	1
DNP Object 20 Default Variation	1
DNP Object 21 Default Variation	1
DNP Object 22 Default Variation	1
DNP Object 23 Default Variation	1
DNP Object 32 Default Variation	1

DNP - IEC104 POINTS LISTS

DNP Binary Input Point 0	START GENERAL On (VO9)
DNP Binary Input Point 1	ON
DNP Binary Input Point 2	ON
DNP Binary Input Point 3	PHASE TOC1 OP
DNP Binary Input Point 4	PHASE TOC2 OP
DNP Binary Input Point 5	PHASE IOC1 OP
DNP Binary Input Point 6	PHASE IOC2 OP
DNP Binary Input Point 7	NEUTRAL TOC1 OP
DNP Binary Input Point 8	NEUTRAL TOC2 OP
DNP Binary Input Point 9	GROUND TOC1 OP
DNP Binary Input Point 10	GROUND TOC2 OP
DNP Binary Input Point 11	ON
DNP Binary Input Point 12	BKR FAIL 1 TRIP OP
DNP Binary Input Point 13	BKR FAIL 1 RETRIP
DNP Binary Input Point 14	ON
DNP Binary Input Point 15	ON
DNP Binary Input Point 16	ON
DNP Binary Input Point 17	FIRST ETHERNET FAIL
DNP Binary Input Point 18	IRIG-b FAILURE
DNP Binary Input Point 19	START GENERA On (VO5)
DNP Binary Input Point 20	RESUMEN TRIP On (VO11)
DNP Binary Input Point 21	52JT2_CE_A On(H1a)
DNP Binary Input Point 22	52JT2_CE_B On(H1c)
DNP Binary Input Point 23	52JT2_CE_C On(H2a)
DNP Binary Input Point 24	52JT2_AB On(H2c)

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VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

DNP - IEC104 POINTS LISTS (continued from last page)

DNP Binary Input Point 25	PS1_PRUEBA On(H3a)
DNP Binary Input Point 26	86T2_OP On(H3c)
DNP Binary Input Point 27	FALLA 52ET2 B1/B2 On(H4a)
DNP Binary Input Point 28	FALLA_PS2/JT2 On(H4c)
DNP Binary Input Point 29	SUP_BIN_PS2/JT2 On(H5a)
DNP Binary Input Point 30	SUP_BIN_BC1/JT2 On(H5c)
DNP Binary Input Point 31	SUP_BIN_PS1/JT2 On(H6a)
DNP Binary Input Point 32	RESERVA On(H6c)
DNP Binary Input Point 33	FALLA_BC1/JT2 On(H7a)
DNP Binary Input Point 34	SUP_RELE_AUX On(H7c)
DNP Binary Input Point 35	52ET2_CE On(H8a)
DNP Binary Input Point 36	52ET2_AB On(H8c)
DNP Binary Input Point 37	86B3_OP On(P5a)
DNP Binary Input Point 38	86BF_JT2_OP On(P5c)
DNP Binary Input Point 39	ARRANQUE_50BF_52JT2 On(P6a)
DNP Binary Input Point 40	ITM_TP/EB1 On(P6c)
DNP Binary Input Point 41	LED ALARM
DNP Binary Input Point 42	LED CURRENT
DNP Binary Input Point 43	LED FREQUENCY
DNP Binary Input Point 44	LED IN SERVICE
DNP Binary Input Point 45	LED NEUTRAL/GROUND
DNP Binary Input Point 46	LED OTHER
DNP Binary Input Point 47	LED PHASE A
DNP Binary Input Point 48	LED PHASE B
DNP Binary Input Point 49	LED PHASE C
DNP Binary Input Point 50	LED PICKUP
DNP Binary Input Point 51	LED TEST IN PROGRESS
DNP Binary Input Point 52	LED TEST MODE
DNP Binary Input Point 53	LED TRIP
DNP Binary Input Point 54	LED TROUBLE
DNP Binary Input Point 55	T° Aceite On(P7a)
DNP Binary Input Point 56	Detector Gas On(P7c)
DNP Binary Input Point 57	Aceite Max On(P8a)
DNP Binary Input Point 58	Aceite Min On(P8c)
DNP Binary Input Point 59	RESET LEDS On (VI1)
DNP Analog Input Point 0	SRC1 Ia RMS
DNP Analog Input Point 1	SRC1 Ib RMS
DNP Analog Input Point 2	SRC1 Ic RMS
DNP Analog Input Point 3	SRC1 In RMS
DNP Analog Input Point 4	SRC1 Ia Mag
DNP Analog Input Point 5	SRC1 Ib Mag
DNP Analog Input Point 6	SRC1 Ic Mag
DNP Analog Input Point 7	SRC1 In Mag
DNP Analog Input Point 8	SRC1 Ig RMS
DNP Analog Input Point 9	SRC1 Ig Mag
DNP Analog Input Point 10	SRC1 I_0 Mag
DNP Analog Input Point 11	SRC1 I_1 Mag
DNP Analog Input Point 12	SRC1 I_2 Mag
DNP Analog Input Point 13	SRC1 Igd Mag
DNP Analog Input Point 14	SRC1 Vag RMS
DNP Analog Input Point 15	SRC1 Vbg RMS
DNP Analog Input Point 16	SRC1 Vcg RMS
DNP Analog Input Point 17	SRC1 Vag Mag
DNP Analog Input Point 18	SRC1 Vbg Mag
DNP Analog Input Point 19	SRC1 Vcg Mag
DNP Analog Input Point 20	SRC1 Vab RMS
DNP Analog Input Point 21	SRC1 Vbc RMS
DNP Analog Input Point 22	SRC1 Vca RMS
DNP Analog Input Point 23	SRC1 Vab Mag
DNP Analog Input Point 24	SRC1 Vbc Mag
DNP Analog Input Point 25	SRC1 Vca Mag
DNP Analog Input Point 26	SRC1 V_0 Mag
DNP Analog Input Point 27	SRC1 V_1 Mag
DNP Analog Input Point 28	SRC1 V_2 Mag
DNP Analog Input Point 29	SRC1 P
DNP Analog Input Point 30	SRC1 Pa
DNP Analog Input Point 31	SRC1 Pb
DNP Analog Input Point 32	SRC1 Pc
DNP Analog Input Point 33	SRC1 Q
DNP Analog Input Point 34	SRC1 Qa

SE LOS VILOS
T60 T2 S1
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VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

DNP - IEC104 POINTS LISTS (continued from last page)

DNP Analog Input Point 35	SRC1	Qb
DNP Analog Input Point 36	SRC1	Qc
DNP Analog Input Point 37	SRC1	S
DNP Analog Input Point 38	SRC1	Sa
DNP Analog Input Point 39	SRC1	Sb
DNP Analog Input Point 40	SRC1	Sc
DNP Analog Input Point 41	SRC1	Pos Watthour
DNP Analog Input Point 42	SRC1	Neg Watthour
DNP Analog Input Point 43	SRC1	Pos varh
DNP Analog Input Point 44	SRC1	Neg varh
DNP Analog Input Point 45	SRC2	Ia RMS
DNP Analog Input Point 46	SRC2	Ib RMS
DNP Analog Input Point 47	SRC2	Ic RMS
DNP Analog Input Point 48	SRC2	In RMS
DNP Analog Input Point 49	SRC2	Ia Mag
DNP Analog Input Point 50	SRC2	Ib Mag
DNP Analog Input Point 51	SRC2	Ic Mag
DNP Analog Input Point 52	SRC2	In Mag
DNP Analog Input Point 53	SRC2	Ig RMS
DNP Analog Input Point 54	SRC2	Ig Mag
DNP Analog Input Point 55	SRC2	I_0 Mag
DNP Analog Input Point 56	SRC2	I_1 Mag
DNP Analog Input Point 57	SRC2	I_2 Mag
DNP Analog Input Point 58	SRC2	Igd Mag
DNP Analog Input Point 59	SRC1	Ia Angle
DNP Analog Input Point 60	SRC1	Ib Angle
DNP Analog Input Point 61	SRC1	Ic Angle
DNP Analog Input Point 62	SRC1	In Angle
DNP Analog Input Point 63	SRC1	Ig Angle
DNP Analog Input Point 64	SRC1	I_0 Angle
DNP Analog Input Point 65	SRC1	I_1 Angle
DNP Analog Input Point 66	SRC1	I_2 Angle
DNP Analog Input Point 67	SRC1	Vag Angle
DNP Analog Input Point 68	SRC1	Vbg Angle
DNP Analog Input Point 69	SRC1	Vcg Angle
DNP Analog Input Point 70	SRC1	Vab Angle
DNP Analog Input Point 71	SRC1	Vbc Angle
DNP Analog Input Point 72	SRC1	Vca Angle
DNP Analog Input Point 73	SRC1	V_0 Angle
DNP Analog Input Point 74	SRC1	V_1 Angle
DNP Analog Input Point 75	SRC1	V_2 Angle
DNP Analog Input Point 76	SRC1	PF
DNP Analog Input Point 77	SRC1	Phase A PF
DNP Analog Input Point 78	SRC1	Phase B PF
DNP Analog Input Point 79	SRC1	Phase C PF
DNP Analog Input Point 80	SRC1	Frequency
DNP Analog Input Point 81	SRC1	Ia THD
DNP Analog Input Point 82	SRC1	Ib THD
DNP Analog Input Point 83	SRC1	Ic THD
DNP Analog Input Point 84	SRC2	Ia Angle
DNP Analog Input Point 85	SRC2	Ib Angle
DNP Analog Input Point 86	SRC2	Ic Angle
DNP Analog Input Point 87	SRC2	In Angle
DNP Analog Input Point 88	SRC2	Ig Angle
DNP Analog Input Point 89	SRC2	I_0 Angle
DNP Analog Input Point 90	SRC2	I_1 Angle
DNP Analog Input Point 91	SRC2	I_2 Angle
DNP Analog Input Point 92	SRC2	Ia THD
DNP Analog Input Point 93	SRC2	Ib THD
DNP Analog Input Point 94	SRC2	Ic THD
DNP Analog Input Point 95	SRC2	Vag RMS
DNP Analog Input Point 96	SRC2	Vbg RMS
DNP Analog Input Point 97	SRC2	Vcg RMS
DNP Analog Input Point 98	SRC2	Vab RMS
DNP Analog Input Point 99	SRC2	Vbc RMS
DNP Analog Input Point 100	SRC2	Vca RMS
DNP Analog Input Point 101	SRC2	P
DNP Analog Input Point 102	SRC2	Pa
DNP Analog Input Point 103	SRC2	Pb
DNP Analog Input Point 104	SRC2	Pc

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

DNP - IEC104 POINTS LISTS (continued from last page)

DNP Analog Input Point 105	SRC2	Q
DNP Analog Input Point 106	SRC2	Qa
DNP Analog Input Point 107	SRC2	Qb
DNP Analog Input Point 108	SRC2	Qc
DNP Analog Input Point 109	SRC2	S
DNP Analog Input Point 110	SRC2	Sa
DNP Analog Input Point 111	SRC2	Sb
DNP Analog Input Point 112	SRC2	Sc
DNP Analog Input Point 113	SRC2	Frequency
DNP Analog Input Point 114	SRC2	PF
DNP Analog Input Point 115	SRC2	Phase A PF
DNP Analog Input Point 116	SRC2	Phase B PF
DNP Analog Input Point 117	SRC2	Phase C PF

IEC 61850

IEC61850:SERVER CONFIGURATION

IED NAME	AA1D01Q04FN3
Access Point	S1
SUBNETWORK1 NAME	W1
SUBNETWORK2 NAME	W2
SUBNETWORK3 NAME	W3
Location	Location
Latitude	0.000 deg
Longitude	0.000 deg
Altitude	0.000 m
LLN0.Mod.ctlModel	sbo-with-normal-security
Configuration Type	G2

IEC61850:REPORTS:REPORT SETTINGS

ReportSettings rptID	Dyn
ReportSettings optFields	Dyn
ReportSettings bufTime	Dyn
ReportSettings trgOps	Dyn
ReportSettings intgPd	Dyn

IEC61850:DATASETS:TT6DATASET1

TT6DataSet01 name	PROT1
TT6DataSet01 shared by	
TT6DataSet01 Member1	Prot.GndPIOC1.ST.Op
TT6DataSet01 Member2	Prot.GndPTOC1.ST.Op
TT6DataSet01 Member3	Prot.NeuPIOC1.ST.Op
TT6DataSet01 Member4	Prot.NeuPIOC2.ST.Op
TT6DataSet01 Member5	Prot.NeuPTOC1.ST.Op
TT6DataSet01 Member6	Prot.NeuPTOC2.ST.Op
TT6DataSet01 Member7	Prot.RgDifPDIF1.ST.Op
TT6DataSet01 Member8	Prot.PctDifPDIF1.ST.Op
TT6DataSet01 Member9	Prot.PiDifPDIF1.ST.Op
TT6DataSet01 Member10	Prot.PhsPIOC1.ST.Op
TT6DataSet01 Member11	Prot.PhsPIOC2.ST.Op
TT6DataSet01 Member12	Prot.PhsPTOC1.ST.Op
TT6DataSet01 Member13	Prot.PhsPTOC2.ST.Op
TT6DataSet01 Member14	Prot.PhsPTOC3.ST.Op

IEC61850:DATASETS:TT6DATASET2

TT6DataSet02 name	PROT2
TT6DataSet02 shared by	
TT6DataSet02 Member1	Master.GGIO1.ST.Ind099
TT6DataSet02 Member2	Master.GGIO1.ST.Ind100
TT6DataSet02 Member3	Master.GGIO1.ST.Ind101
TT6DataSet02 Member4	Master.GGIO1.ST.Ind102
TT6DataSet02 Member5	Master.GGIO1.ST.Ind079
TT6DataSet02 Member6	Master.GGIO1.ST.Ind080
TT6DataSet02 Member7	Master.GGIO1.ST.Ind081
TT6DataSet02 Member8	Master.GGIO1.ST.Ind082

IEC61850:DATASETS:TT6DATASET3

TT6DataSet03 name	BIN1
TT6DataSet03 shared by	
TT6DataSet03 Member1	Master.GGIO1.ST.Ind001
TT6DataSet03 Member2	Master.GGIO1.ST.Ind002

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T60 T2 S1
DEVICE DEFINITION
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VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

IEC61850:DATASETS:TT6DataSet3 (continued from last page)

TT6DataSet03 Member3	Master.GGIO1.ST.Ind003
TT6DataSet03 Member4	Master.GGIO1.ST.Ind004
TT6DataSet03 Member5	Master.GGIO1.ST.Ind005
TT6DataSet03 Member6	Master.GGIO1.ST.Ind006
TT6DataSet03 Member7	Master.GGIO1.ST.Ind007
TT6DataSet03 Member8	Master.GGIO1.ST.Ind008
TT6DataSet03 Member9	Master.GGIO1.ST.Ind009
TT6DataSet03 Member10	Master.GGIO1.ST.Ind010
TT6DataSet03 Member11	Master.GGIO1.ST.Ind011
TT6DataSet03 Member12	Master.GGIO1.ST.Ind012
TT6DataSet03 Member13	Master.GGIO1.ST.Ind013
TT6DataSet03 Member14	Master.GGIO1.ST.Ind014
TT6DataSet03 Member15	Master.GGIO1.ST.Ind015
TT6DataSet03 Member16	Master.GGIO1.ST.Ind016
TT6DataSet03 Member17	Master.GGIO1.ST.Ind017
TT6DataSet03 Member18	Master.GGIO1.ST.Ind018
TT6DataSet03 Member19	Master.GGIO1.ST.Ind019
TT6DataSet03 Member20	Master.GGIO1.ST.Ind020

IEC61850:DATASETS:TT6DATASET4

TT6DataSet04 name	BIN2
TT6DataSet04 shared by	
TT6DataSet04 Member1	Master.LPHD1.ST.Eth1Fail
TT6DataSet04 Member2	Master.LPHD1.ST.Eth2Fail
TT6DataSet04 Member3	Master.LPHD1.ST.Eth3Fail
TT6DataSet04 Member4	Master.LPHD1.ST.PhyHealth
TT6DataSet04 Member5	Master.LPHD1.ST.AnyErr
TT6DataSet04 Member6	Master.LPHD1.ST.ClkUnsynch
TT6DataSet04 Member7	Master.LPHD1.ST.IrigbFail

IEC61850:DATASETS:TT6DATASET5

TT6DataSet05 name	MMX
TT6DataSet05 shared by	
TT6DataSet05 Member1	Prot.PctDifPDIF1.MX.DifAClc.phsA
TT6DataSet05 Member2	Prot.PctDifPDIF1.MX.DifAClc.phsB
TT6DataSet05 Member3	Prot.PctDifPDIF1.MX.DifAClc.phsC
TT6DataSet05 Member4	Prot.PctDifPDIF1.MX.RstA.phsA
TT6DataSet05 Member5	Prot.PctDifPDIF1.MX.RstA.phsB
TT6DataSet05 Member6	Prot.PctDifPDIF1.MX.RstA.phsC
TT6DataSet05 Member7	Prot.RgDifPDIF1.MX.DifAClc
TT6DataSet05 Member8	Prot.RgDifPDIF1.MX.RstA

IEC61850:DATASETS:TT3DATASET01

TT3DataSet01 name	TT3_BIN3
TT3DataSet01 shared by	
TT3DataSet01 Member1	Master.GGIO1.ST.Ind021
TT3DataSet01 Member2	Master.GGIO1.ST.Ind022
TT3DataSet01 Member3	Master.GGIO1.ST.Ind023
TT3DataSet01 Member4	Master.GGIO1.ST.Ind024
TT3DataSet01 Member5	Master.GGIO1.ST.Ind025
TT3DataSet01 Member6	Master.GGIO1.ST.Ind026
TT3DataSet01 Member7	Master.GGIO1.ST.Ind027
TT3DataSet01 Member8	Master.GGIO1.ST.Ind028
TT3DataSet01 Member9	Master.GGIO1.ST.Ind029
TT3DataSet01 Member10	Master.GGIO1.ST.Ind030
TT3DataSet01 Member11	Master.GGIO1.ST.Ind031
TT3DataSet01 Member12	Master.GGIO1.ST.Ind032
TT3DataSet01 Member13	Master.GGIO1.ST.Ind033
TT3DataSet01 Member14	Master.GGIO1.ST.Ind034
TT3DataSet01 Member15	Master.GGIO1.ST.Ind035
TT3DataSet01 Member16	Master.GGIO1.ST.Ind036
TT3DataSet01 Member17	Master.GGIO1.ST.Ind037
TT3DataSet01 Member18	Master.GGIO1.ST.Ind038
TT3DataSet01 Member19	Master.GGIO1.ST.Ind039
TT3DataSet01 Member20	Master.GGIO1.ST.Ind040

IEC61850:DATASETS:TT3DATASET02

TT3DataSet02 name	TT3_BIN4
TT3DataSet02 shared by	
TT3DataSet02 Member1	Master.GGIO1.ST.Ind041

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IEC61850:DATASETS:TT3DataSet02 (continued from last page)

TT3DataSet02 Member2	Master.GGIO1.ST.Ind042
TT3DataSet02 Member3	Master.GGIO1.ST.Ind043
TT3DataSet02 Member4	Master.GGIO1.ST.Ind044
TT3DataSet02 Member5	Master.GGIO1.ST.Ind045
TT3DataSet02 Member6	Master.GGIO1.ST.Ind046
TT3DataSet02 Member7	Master.GGIO1.ST.Ind047
TT3DataSet02 Member8	Master.GGIO1.ST.Ind048
TT3DataSet02 Member9	Master.GGIO1.ST.Ind049
TT3DataSet02 Member10	Master.GGIO1.ST.Ind050
TT3DataSet02 Member11	Master.GGIO1.ST.Ind051
TT3DataSet02 Member12	Master.GGIO1.ST.Ind052
TT3DataSet02 Member13	Master.GGIO1.ST.Ind053
TT3DataSet02 Member14	Master.GGIO1.ST.Ind054
TT3DataSet02 Member15	Master.GGIO1.ST.Ind055
TT3DataSet02 Member16	Master.GGIO1.ST.Ind056
TT3DataSet02 Member17	Master.GGIO1.ST.Ind057
TT3DataSet02 Member18	Master.GGIO1.ST.Ind058
TT3DataSet02 Member19	Master.GGIO1.ST.Ind059
TT3DataSet02 Member20	Master.GGIO1.ST.Ind060

IEC61850:DATASETS:TT3DATASET05

TT3DataSet05 name	TT3DataSet05_TRAFO_MMX
TT3DataSet05 shared by	
TT3DataSet05 Member1	Meter.ACsrcMMXU1.MX.TotW
TT3DataSet05 Member2	Meter.ACsrcMMXU1.MX.TotVAr
TT3DataSet05 Member3	Meter.ACsrcMMXU1.MX.TotVA
TT3DataSet05 Member4	Meter.ACsrcMMXU1.MX.TotPF
TT3DataSet05 Member5	Meter.ACsrcMMXU1.MX.Hz
TT3DataSet05 Member6	Meter.ACsrcMMXU1.MX.VaRMS
TT3DataSet05 Member7	Meter.ACsrcMMXU1.MX.VbRMS
TT3DataSet05 Member8	Meter.ACsrcMMXU1.MX.VcRMS
TT3DataSet05 Member9	Meter.ACsrcMMXU1.MX.AaRMS
TT3DataSet05 Member10	Meter.ACsrcMMXU1.MX.AbRMS
TT3DataSet05 Member11	Meter.ACsrcMMXU1.MX.AcRMS
TT3DataSet05 Member12	Meter.ACsrcMMXU1.MX.AresRMS

IEC61850:SYSTEM_SETUP:SIGNAL_SOURCES:SOURCE_1:SOURCE 1 DEADBANDS

MMXU1 TotW DEADBAND	0.000 %
MMXU1 TotVAr DEADBAND	0.000 %
MMXU1 TotVA DEADBAND	0.000 %
MMXU1 TotPF DEADBAND	0.000 %
MMXU1 Hz DEADBAND	0.000 %
MMXU1 PPV.phsAB.db	0.000 %
MMXU1 PPV.phsBC.db	0.000 %
MMXU1 PPV.phsCA.db	0.000 %
MMXU1 RMS PPV.phsAB DEADBAND	0.000 %
MMXU1 RMS PPV.phsBC DEADBAND	0.000 %
MMXU1 RMS PPV.phsCA DEADBAND	0.000 %
MMXU1 PhV.phsA.db	0.000 %
MMXU1 PhV.phsB.db	0.000 %
MMXU1 PhV.phsC.db	0.000 %
MMXU1 RMS PhV.phsA.db	0.000 %
MMXU1 RMS PhV.phsB.db	0.000 %
MMXU1 RMS PhV.phsC.db	0.000 %
MMXU1 A.phsA DEADBAND	0.000 %
MMXU1 A.phsB DEADBAND	0.000 %
MMXU1 A.phsC DEADBAND	0.000 %
MMXU1 A.neut.db	0.000 %
MMXU1 RMS A.phsA DEADBAND	0.000 %
MMXU1 RMS A.phsB DEADBAND	0.000 %
MMXU1 RMS A.phsC DEADBAND	0.000 %
MMXU1 RMS A.res DEADBAND	0.000 %
MMXU1 Dif Gnd Cur.db	0.000 %
MMXU1 W.phsA DEADBAND	0.000 %
MMXU1 W.phsB DEADBAND	0.000 %
MMXU1 W.phsC DEADBAND	0.000 %
MMXU1 VAr.phsA DEADBAND	0.000 %
MMXU1 VAr.phsB DEADBAND	0.000 %
MMXU1 VAr.phsC DEADBAND	0.000 %
MMXU1 VA.phsA DEADBAND	0.000 %

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

IEC61850:SYSTEM_SETUP:SIGNAL_SOURCES:SOURCE_1:Source 1 Deadbands (continued from last page)

MMXU1 VA.phsB DEADBAND	0.000 %
MMXU1 VA.phsC DEADBAND	0.000 %
MMXU1 PF.phsA DEADBAND	0.000 %
MMXU1 PF.phsB DEADBAND	0.000 %
MMXU1 PF.phsC DEADBAND	0.000 %
MMXN1 AuxV.db	0.000 %
MMXN1 RMS AuxV DEADBAND	0.000 %
MMXN1 GndCur.db	0.000 %
MMXN1 RMS GndCur.db	0.000 %
MSQI1 Seq1 Cur.db	0.000 %
MSQI1 Seq2 Cur.db	0.000 %
MSQI1 Seq0 Cur.db	0.000 %
MSQI1 Seq1 V.db	0.000 %
MSQI1 Seq2 V.db	0.000 %
MSQI1 Seq0 V.db	0.000 %

IEC61850:SYSTEM_SETUP:SIGNAL_SOURCES:SOURCE_2:SOURCE 2 DEADBANDS

MMXU2 TotW DEADBAND	0.000 %
MMXU2 TotVAr DEADBAND	0.000 %
MMXU2 TotVA DEADBAND	0.000 %
MMXU2 TotPF DEADBAND	0.000 %
MMXU2 Hz DEADBAND	0.000 %
MMXU2 PPV.phsAB.db	0.000 %
MMXU2 PPV.phsBC.db	0.000 %
MMXU2 PPV.phsCA.db	0.000 %
MMXU2 RMS PPV.phsAB DEADBAND	0.000 %
MMXU2 RMS PPV.phsBC DEADBAND	0.000 %
MMXU2 RMS PPV.phsCA DEADBAND	0.000 %
MMXU2 PhV.phsA.db	0.000 %
MMXU2 PhV.phsB.db	0.000 %
MMXU2 PhV.phsC.db	0.000 %
MMXU2 RMS PhV.phsA.db	0.000 %
MMXU2 RMS PhV.phsB.db	0.000 %
MMXU2 RMS PhV.phsC.db	0.000 %
MMXU2 A.phsA DEADBAND	0.000 %
MMXU2 A.phsB DEADBAND	0.000 %
MMXU2 A.phsC DEADBAND	0.000 %
MMXU2 A.neut.db	0.000 %
MMXU2 RMS A.phsA DEADBAND	0.000 %
MMXU2 RMS A.phsB DEADBAND	0.000 %
MMXU2 RMS A.phsC DEADBAND	0.000 %
MMXU2 RMS A.res DEADBAND	0.000 %
MMXU2 Dif Gnd Cur.db	0.000 %
MMXU2 W.phsA DEADBAND	0.000 %
MMXU2 W.phsB DEADBAND	0.000 %
MMXU2 W.phsC DEADBAND	0.000 %
MMXU2 VAr.phsA DEADBAND	0.000 %
MMXU2 VAr.phsB DEADBAND	0.000 %
MMXU2 VAr.phsC DEADBAND	0.000 %
MMXU2 VA.phsA DEADBAND	0.000 %
MMXU2 VA.phsB DEADBAND	0.000 %
MMXU2 VA.phsC DEADBAND	0.000 %
MMXU2 PF.phsA DEADBAND	0.000 %
MMXU2 PF.phsB DEADBAND	0.000 %
MMXU2 PF.phsC DEADBAND	0.000 %
MMXN2 AuxV.db	0.000 %
MMXN2 RMS AuxV DEADBAND	0.000 %
MMXN2 GndCur.db	0.000 %
MMXN2 RMS GndCur.db	0.000 %
MSQI2 Seq1 Cur.db	0.000 %
MSQI2 Seq2 Cur.db	0.000 %
MSQI2 Seq0 Cur.db	0.000 %
MSQI2 Seq1 V.db	0.000 %
MSQI2 Seq2 V.db	0.000 %
MSQI2 Seq0 V.db	0.000 %

IEC61850:SYSTEM_SETUP:SIGNAL_SOURCES:SOURCE_3:SOURCE 3 DEADBANDS

MMXU3 TotW DEADBAND	0.000 %
MMXU3 TotVAr DEADBAND	0.000 %
MMXU3 TotVA DEADBAND	0.000 %

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

IEC61850:SYSTEM_SETUP:SIGNAL_SOURCES:SOURCE_3:Source 3 Deadbands (continued from last page)

MMXU3 TotPF DEADBAND	0.000 %
MMXU3 Hz DEADBAND	0.000 %
MMXU3 PPV.phsAB.db	0.000 %
MMXU3 PPV.phsBC.db	0.000 %
MMXU3 PPV.phsCA.db	0.000 %
MMXU3 RMS PPV.phsAB DEADBAND	0.000 %
MMXU3 RMS PPV.phsBC DEADBAND	0.000 %
MMXU3 RMS PPV.phsCA DEADBAND	0.000 %
MMXU3 PhV.phsA.db	0.000 %
MMXU3 PhV.phsB.db	0.000 %
MMXU3 PhV.phsC.db	0.000 %
MMXU3 RMS PhV.phsA.db	0.000 %
MMXU3 RMS PhV.phsB.db	0.000 %
MMXU3 RMS PhV.phsC.db	0.000 %
MMXU3 A.phsA DEADBAND	0.000 %
MMXU3 A.phsB DEADBAND	0.000 %
MMXU3 A.phsC DEADBAND	0.000 %
MMXU3 A.neut.db	0.000 %
MMXU3 RMS A.phsA DEADBAND	0.000 %
MMXU3 RMS A.phsB DEADBAND	0.000 %
MMXU3 RMS A.phsC DEADBAND	0.000 %
MMXU3 RMS A.res DEADBAND	0.000 %
MMXU3 Dif Gnd Cur.db	0.000 %
MMXU3 W.phsA DEADBAND	0.000 %
MMXU3 W.phsB DEADBAND	0.000 %
MMXU3 W.phsC DEADBAND	0.000 %
MMXU3 VAr.phsA DEADBAND	0.000 %
MMXU3 VAr.phsB DEADBAND	0.000 %
MMXU3 VAr.phsC DEADBAND	0.000 %
MMXU3 VA.phsA DEADBAND	0.000 %
MMXU3 VA.phsB DEADBAND	0.000 %
MMXU3 VA.phsC DEADBAND	0.000 %
MMXU3 PF.phsA DEADBAND	0.000 %
MMXU3 PF.phsB DEADBAND	0.000 %
MMXU3 PF.phsC DEADBAND	0.000 %
MMXN3 AuxV.db	0.000 %
MMXN3 RMS AuxV DEADBAND	0.000 %
MMXN3 GndCur.db	0.000 %
MMXN3 RMS GndCur.db	0.000 %
MSQI3 Seq1 Cur.db	0.000 %
MSQI3 Seq2 Cur.db	0.000 %
MSQI3 Seq0 Cur.db	0.000 %
MSQI3 Seq1 V.db	0.000 %
MSQI3 Seq2 V.db	0.000 %
MSQI3 Seq0 V.db	0.000 %

IEC61850:SYSTEM_SETUP:SIGNAL_SOURCES:SOURCE_4:SOURCE 4 DEADBANDS

MMXU4 TotW DEADBAND	0.000 %
MMXU4 TotVAr DEADBAND	0.000 %
MMXU4 TotVA DEADBAND	0.000 %
MMXU4 TotPF DEADBAND	0.000 %
MMXU4 Hz DEADBAND	0.000 %
MMXU4 PPV.phsAB.db	0.000 %
MMXU4 PPV.phsBC.db	0.000 %
MMXU4 PPV.phsCA.db	0.000 %
MMXU4 RMS PPV.phsAB DEADBAND	0.000 %
MMXU4 RMS PPV.phsBC DEADBAND	0.000 %
MMXU4 RMS PPV.phsCA DEADBAND	0.000 %
MMXU4 PhV.phsA.db	0.000 %
MMXU4 PhV.phsB.db	0.000 %
MMXU4 PhV.phsC.db	0.000 %
MMXU4 RMS PhV.phsA.db	0.000 %
MMXU4 RMS PhV.phsB.db	0.000 %
MMXU4 RMS PhV.phsC.db	0.000 %
MMXU4 A.phsA DEADBAND	0.000 %
MMXU4 A.phsB DEADBAND	0.000 %
MMXU4 A.phsC DEADBAND	0.000 %
MMXU4 A.neut.db	0.000 %
MMXU4 RMS A.phsA DEADBAND	0.000 %
MMXU4 RMS A.phsB DEADBAND	0.000 %

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

IEC61850:SYSTEM_SETUP:SIGNAL_SOURCES:SOURCE_4:Source 4 Deadbands (continued from last page)

MMXU4 RMS A.phsC DEADBAND	0.000 %
MMXU4 RMS A.res DEADBAND	0.000 %
MMXU4 Dif Gnd Cur.db	0.000 %
MMXU4 W.phsA DEADBAND	0.000 %
MMXU4 W.phsB DEADBAND	0.000 %
MMXU4 W.phsC DEADBAND	0.000 %
MMXU4 VAr.phsA DEADBAND	0.000 %
MMXU4 VAr.phsB DEADBAND	0.000 %
MMXU4 VAr.phsC DEADBAND	0.000 %
MMXU4 VA.phsA DEADBAND	0.000 %
MMXU4 VA.phsB DEADBAND	0.000 %
MMXU4 VA.phsC DEADBAND	0.000 %
MMXU4 PF.phsA DEADBAND	0.000 %
MMXU4 PF.phsB DEADBAND	0.000 %
MMXU4 PF.phsC DEADBAND	0.000 %
MMXN4 AuxV.db	0.000 %
MMXN4 RMS AuxV DEADBAND	0.000 %
MMXN4 GndCur.db	0.000 %
MMXN4 RMS GndCur.db	0.000 %
MSQI4 Seq1 Cur.db	0.000 %
MSQI4 Seq2 Cur.db	0.000 %
MSQI4 Seq0 Cur.db	0.000 %
MSQI4 Seq1 V.db	0.000 %
MSQI4 Seq2 V.db	0.000 %
MSQI4 Seq0 V.db	0.000 %

IEC61850:SYSTEM_SETUP:BREAKERS:BREAKER 1

XCBR1 Pos ctlModel	sbo-with-enhanced-security
XCBR1 Pos sboTimeout	30 s
XCBR1 BlkOpn ctlModel	direct-with-normal-security
XCBR1 BlkCls ctlModel	direct-with-normal-security
CSWI1 CmdBlk ctlModel	direct-with-normal-security
CSWI1 Pos ctlModel	direct-with-normal-security
CSWI1 Pos sboTimeout	30 s
CSWI1 BlkOpn ctlModel	direct-with-normal-security
CSWI1 BlkCls ctlModel	direct-with-normal-security

IEC61850:SYSTEM_SETUP:BREAKERS:BREAKER 2

XCBR2 Pos ctlModel	sbo-with-enhanced-security
XCBR2 Pos sboTimeout	30 s
XCBR2 BlkOpn ctlModel	direct-with-normal-security
XCBR2 BlkCls ctlModel	direct-with-normal-security
CSWI2 CmdBlk ctlModel	direct-with-normal-security
CSWI2 Pos ctlModel	direct-with-normal-security
CSWI2 Pos sboTimeout	30 s
CSWI2 BlkOpn ctlModel	direct-with-normal-security
CSWI2 BlkCls ctlModel	direct-with-normal-security

IEC61850:SYSTEM_SETUP:SWITCHES:SWITCH 1

XSWI1 Pos ctlModel	sbo-with-enhanced-security
XSWI1 Pos sboTimeout	30 s
Disc0XSWI1 BlkOpn ctlModel	direct-with-normal-security
Disc0XSWI1 BlkCls ctlModel	direct-with-normal-security
DiscCSWI1 Pos ctlModel	direct-with-normal-security
DiscCSWI1 Pos sboTimeout	30 s

IEC61850:SYSTEM_SETUP:SWITCHES:SWITCH 2

XSWI2 Pos ctlModel	sbo-with-enhanced-security
XSWI2 Pos sboTimeout	30 s
Disc0XSWI2 BlkOpn ctlModel	direct-with-normal-security
Disc0XSWI2 BlkCls ctlModel	direct-with-normal-security
DiscCSWI2 Pos ctlModel	direct-with-normal-security
DiscCSWI2 Pos sboTimeout	30 s

IEC61850:SYSTEM_SETUP:SWITCHES:SWITCH 3

XSWI3 Pos ctlModel	sbo-with-enhanced-security
XSWI3 Pos sboTimeout	30 s
Disc0XSWI3 BlkOpn ctlModel	direct-with-normal-security
Disc0XSWI3 BlkCls ctlModel	direct-with-normal-security
DiscCSWI3 Pos ctlModel	direct-with-normal-security

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

IEC61850:SYSTEM_SETUP:SWITCHES:Switch 3 (continued from last page)

DiscCSWI3 Pos sboTimeout	30 s

IEC61850:SYSTEM_SETUP:SWITCHES:SWITCH 4

XSWI4 Pos ctlModel	sbo-with-enhanced-security
XSWI4 Pos sboTimeout	30 s
Disc0XSWI4 BlkOpn ctlModel	direct-with-normal-security
Disc0XSWI4 BlkCls ctlModel	direct-with-normal-security
DiscCSWI4 Pos ctlModel	direct-with-normal-security
DiscCSWI4 Pos sboTimeout	30 s

IEC61850:SYSTEM_SETUP:SWITCHES:SWITCH 5

XSWI5 Pos ctlModel	sbo-with-enhanced-security
XSWI5 Pos sboTimeout	30 s
Disc0XSWI5 BlkOpn ctlModel	direct-with-normal-security
Disc0XSWI5 BlkCls ctlModel	direct-with-normal-security
DiscCSWI5 Pos ctlModel	direct-with-normal-security
DiscCSWI5 Pos sboTimeout	30 s

IEC61850:SYSTEM_SETUP:SWITCHES:SWITCH 6

XSWI6 Pos ctlModel	sbo-with-enhanced-security
XSWI6 Pos sboTimeout	30 s
Disc0XSWI6 BlkOpn ctlModel	direct-with-normal-security
Disc0XSWI6 BlkCls ctlModel	direct-with-normal-security
DiscCSWI6 Pos ctlModel	direct-with-normal-security
DiscCSWI6 Pos sboTimeout	30 s

IEC61850:SYSTEM_SETUP:SWITCHES:SWITCH 7

XSWI7 Pos ctlModel	sbo-with-enhanced-security
XSWI7 Pos sboTimeout	30 s
Disc0XSWI7 BlkOpn ctlModel	direct-with-normal-security
Disc0XSWI7 BlkCls ctlModel	direct-with-normal-security
DiscCSWI7 Pos ctlModel	direct-with-normal-security
DiscCSWI7 Pos sboTimeout	30 s

IEC61850:SYSTEM_SETUP:SWITCHES:SWITCH 8

XSWI8 Pos ctlModel	sbo-with-enhanced-security
XSWI8 Pos sboTimeout	30 s
Disc0XSWI8 BlkOpn ctlModel	direct-with-normal-security
Disc0XSWI8 BlkCls ctlModel	direct-with-normal-security
DiscCSWI8 Pos ctlModel	direct-with-normal-security
DiscCSWI8 Pos sboTimeout	30 s

IEC61850:GROUPED_ELEMENTS:PERCENT DIFFERENTIAL

PctDifPDIF1.DifAClc.phsA.db	0.000 %
PctDifPDIF1.RstA.phsA.db	0.000 %
PctDifPDIF1.DifA2ndH.phsA.db	0.000 %
PctDifPDIF1.DifA5thH.phsA.db	0.000 %
PctDifPDIF1.DifAClc.phsB.db	0.000 %
PctDifPDIF1.RstA.phsB.db	0.000 %
PctDifPDIF1.DifA2ndH.phsB.db	0.000 %
PctDifPDIF1.DifA5thH.phsB.db	0.000 %
PctDifPDIF1.DifAClc.phsC.db	0.000 %
PctDifPDIF1.RstA.phsC.db	0.000 %
PctDifPDIF1.DifA2ndH.phsC.db	0.000 %
PctDifPDIF1.DifA5thH.phsC.db	0.000 %

IEC61850:GROUPED_ELEMENTS:RESTRICTED GROUND FAULT

RgDifPDIF1.DifAClc.net.db	0.000 %
RgDifPDIF1.RstA.net.db	0.000 %
RgDifPDIF2.DifAClc.net.db	0.000 %
RgDifPDIF2.RstA.net.db	0.000 %

IEC61850:CONTROL_ELEMENTS:SETTING GROUPS

Initial Setting Group	1

IEC61850:CONTROL_ELEMENTS:SYNCHROCHECK:SYNCHROCHECK 1

SyChkRSYN1.DifVClc.db	0.000 %
SyChkRSYN1.DifHzClc.db	0.000 %
SyChkRSYN1.DifAngClc.db	0.000 %
SyChkRSYN1.DifAngDis.db	0.000 %

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

IEC61850:CONTROL_ELEMENTS:SYNCHROCHECK:Synchrocheck 1 (continued from last page)

SyChkRSYN1.ProjDifDis.db	0.000 %
SyChkRSYN1.V1Src.db	0.000 %
SyChkRSYN1.V2Src.db	0.000 %
SyChkRSYN1.ProjV2.db	0.000 %
SyChkRSYN1.ProjDif.db	0.000 %

IEC61850:CONTROL_ELEMENTS:SYNCHROCHECK:SYNCHROCHECK 2

SyChkRSYN2.DifVClc.db	0.000 %
SyChkRSYN2.DifHzClc.db	0.000 %
SyChkRSYN2.DifAngClc.db	0.000 %
SyChkRSYN2.DifAngDis.db	0.000 %
SyChkRSYN2.ProjDifDis.db	0.000 %
SyChkRSYN2.V1Src.db	0.000 %
SyChkRSYN2.V2Src.db	0.000 %
SyChkRSYN2.ProjV2.db	0.000 %
SyChkRSYN2.ProjDif.db	0.000 %

IEC61850:CONTROL_ELEMENTS:SYNCHROCHECK:SYNCHROCHECK 3

SyChkRSYN3.DifVClc.db	0.000 %
SyChkRSYN3.DifHzClc.db	0.000 %
SyChkRSYN3.DifAngClc.db	0.000 %
SyChkRSYN3.DifAngDis.db	0.000 %
SyChkRSYN3.ProjDifDis.db	0.000 %
SyChkRSYN3.V1Src.db	0.000 %
SyChkRSYN3.V2Src.db	0.000 %
SyChkRSYN3.ProjV2.db	0.000 %
SyChkRSYN3.ProjDif.db	0.000 %

IEC61850:CONTROL_ELEMENTS:SYNCHROCHECK:SYNCHROCHECK 4

SyChkRSYN4.DifVClc.db	0.000 %
SyChkRSYN4.DifHzClc.db	0.000 %
SyChkRSYN4.DifAngClc.db	0.000 %
SyChkRSYN4.DifAngDis.db	0.000 %
SyChkRSYN4.ProjDifDis.db	0.000 %
SyChkRSYN4.V1Src.db	0.000 %
SyChkRSYN4.V2Src.db	0.000 %
SyChkRSYN4.ProjV2.db	0.000 %
SyChkRSYN4.ProjDif.db	0.000 %

IEC61850:CONTROL_ELEMENTS:SYNCHROCHECK:SYNCHROCHECK 5

SyChkRSYN5.DifVClc.db	0.000 %
SyChkRSYN5.DifHzClc.db	0.000 %
SyChkRSYN5.DifAngClc.db	0.000 %
SyChkRSYN5.DifAngDis.db	0.000 %
SyChkRSYN5.ProjDifDis.db	0.000 %
SyChkRSYN5.V1Src.db	0.000 %
SyChkRSYN5.V2Src.db	0.000 %
SyChkRSYN5.ProjV2.db	0.000 %
SyChkRSYN5.ProjDif.db	0.000 %

IEC61850:CONTROL_ELEMENTS:SYNCHROCHECK:SYNCHROCHECK 6

SyChkRSYN6.DifVClc.db	0.000 %
SyChkRSYN6.DifHzClc.db	0.000 %
SyChkRSYN6.DifAngClc.db	0.000 %
SyChkRSYN6.DifAngDis.db	0.000 %
SyChkRSYN6.ProjDifDis.db	0.000 %
SyChkRSYN6.V1Src.db	0.000 %
SyChkRSYN6.V2Src.db	0.000 %
SyChkRSYN6.ProjV2.db	0.000 %
SyChkRSYN6.ProjDif.db	0.000 %

IEC61850:SETTINGS FOR COMMANDS

LLN0.EvtRcdClr.ctlModel	direct-with-normal-security
LPHD1.RsStat.ctlModel	direct-with-normal-security
OscRDRE1.RcdTrg.ctlModel	direct-with-normal-security
OscRDRE1.MemClr.ctlModel	direct-with-normal-security

IEC61850:GGIO:GGIO1

GGIO1 INDICATION 1	52JT2_CE_A On(H1a)
GGIO1 INDICATION 2	52JT2_CE_B On(H1c)

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

IEC61850:GGIO:GGIO1 (continued from last page)

GGIO1 INDICATION 3	52JT2_CE_C On(H2a)
GGIO1 INDICATION 4	52JT2_AB On(H2c)
GGIO1 INDICATION 5	PS1_PRUEBA On(H3a)
GGIO1 INDICATION 6	86T2_OP On(H3c)
GGIO1 INDICATION 7	FALLA 52ET2 B1/B2 On(H4a)
GGIO1 INDICATION 8	FALLA_PS2/JT2 On(H4c)
GGIO1 INDICATION 9	SUP_BIN_PS2/JT2 On(H5a)
GGIO1 INDICATION 10	SUP_BIN_BC1/JT2 On(H5c)
GGIO1 INDICATION 11	SUP_BIN_PS1/JT2 On(H6a)
GGIO1 INDICATION 12	RESERVA On(H6c)
GGIO1 INDICATION 13	FALLA_BC1/JT2 On(H7a)
GGIO1 INDICATION 14	SUP_RELE_AUX On(H7c)
GGIO1 INDICATION 15	52ET2_CE On(H8a)
GGIO1 INDICATION 16	52ET2_AB On(H8c)
GGIO1 INDICATION 17	86B3_OP On(P5a)
GGIO1 INDICATION 18	86BF_JT2_OP On(P5c)
GGIO1 INDICATION 19	ARRANQUE_50BF_52JT2 On(P6a)
GGIO1 INDICATION 20	ITM_TP/EB1 On(P6c)
GGIO1 INDICATION 21	T° Aceite On(P7a)
GGIO1 INDICATION 22	Detector Gas On(P7c)
GGIO1 INDICATION 23	Aceite Max On(P8a)
GGIO1 INDICATION 24	Aceite Min On(P8c)
GGIO1 INDICATION 35	86B3_OP On(P5a)
GGIO1 INDICATION 36	T° Aceite On(P7a)
GGIO1 INDICATION 37	Aceite Max On(P8a)
GGIO1 INDICATION 79	LED TRIP
GGIO1 INDICATION 80	LED PHASE A
GGIO1 INDICATION 81	LED PHASE B
GGIO1 INDICATION 82	LED PHASE C
GGIO1 INDICATION 83	XFMR INST DIFF OP
GGIO1 INDICATION 84	XFMR PCNT DIFF OP
GGIO1 INDICATION 85	PHASE TOC1 OP
GGIO1 INDICATION 86	PHASE TOC2 OP
GGIO1 INDICATION 87	PHASE TOC3 OP
GGIO1 INDICATION 88	PHASE IOC1 OP
GGIO1 INDICATION 89	PHASE IOC2 OP
GGIO1 INDICATION 90	PHASE IOC3 OP
GGIO1 INDICATION 91	NEUTRAL TOC1 OP
GGIO1 INDICATION 92	NEUTRAL TOC2 OP
GGIO1 INDICATION 93	NEUTRAL TOC3 OP
GGIO1 INDICATION 94	NEUTRAL IOC1 OP
GGIO1 INDICATION 95	NEUTRAL IOC2 OP
GGIO1 INDICATION 96	NEUTRAL IOC3 OP
GGIO1 INDICATION 97	GROUND TOC1 OP
GGIO1 INDICATION 98	GROUND TOC2 OP
GGIO1 INDICATION 99	BKR FAIL 1 RETRIP
GGIO1 INDICATION 100	BKR FAIL 1 TRIP OP
GGIO1 INDICATION 101	BKR FAIL 2 RETRIP
GGIO1 INDICATION 102	BKR FAIL 2 TRIP OP

IEC61850:GGIO:GGIO2

GGIO2 CF SPCSO 1 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 2 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 3 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 4 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 5 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 6 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 7 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 8 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 9 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 10 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 11 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 12 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 13 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 14 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 15 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 16 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 17 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 18 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 19 CTLMODEL	direct-with-normal-security

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

IEC61850:GGIO:GGIO2 (continued from last page)

GGIO2 CF SPCSO 20 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 21 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 22 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 23 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 24 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 25 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 26 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 27 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 28 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 29 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 30 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 31 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 32 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 33 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 34 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 35 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 36 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 37 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 38 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 39 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 40 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 41 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 42 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 43 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 44 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 45 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 46 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 47 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 48 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 49 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 50 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 51 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 52 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 53 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 54 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 55 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 56 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 57 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 58 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 59 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 60 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 61 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 62 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 63 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 64 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 65 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 66 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 67 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 68 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 69 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 70 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 71 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 72 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 73 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 74 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 75 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 76 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 77 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 78 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 79 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 80 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 81 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 82 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 83 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 84 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 85 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 86 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 87 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 88 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 89 CTLMODEL	direct-with-normal-security

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

IEC61850:GGIO:GGIO2 (continued from last page)

GGIO2 CF SPCSO 90 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 91 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 92 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 93 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 94 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 95 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 96 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 97 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 98 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 99 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 100 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 101 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 102 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 103 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 104 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 105 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 106 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 107 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 108 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 109 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 110 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 111 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 112 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 113 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 114 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 115 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 116 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 117 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 118 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 119 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 120 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 121 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 122 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 123 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 124 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 125 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 126 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 127 CTLMODEL	direct-with-normal-security
GGIO2 CF SPCSO 128 CTLMODEL	direct-with-normal-security

IEC61850:GGIO:GGIO4:GGIO4.ANIN1

ANALOG IN 1 VALUE	SRC1 Ia Mag
ANALOG IN 1 DB	0.000 %
ANALOG IN 1 MIN	0.000
ANALOG IN 1 MAX	1000000.000

IEC61850:GGIO:GGIO4:GGIO4.ANIN2

ANALOG IN 2 VALUE	SRC1 Ib Mag
ANALOG IN 2 DB	0.000 %
ANALOG IN 2 MIN	0.000
ANALOG IN 2 MAX	1000000.000

IEC61850:GGIO:GGIO4:GGIO4.ANIN3

ANALOG IN 3 VALUE	SRC1 Ic Mag
ANALOG IN 3 DB	0.000 %
ANALOG IN 3 MIN	0.000
ANALOG IN 3 MAX	1000000.000

IEC61850:GGIO:GGIO4:GGIO4.ANIN4

ANALOG IN 4 VALUE	SRC1 In Mag
ANALOG IN 4 DB	0.000 %
ANALOG IN 4 MIN	0.000
ANALOG IN 4 MAX	1000000.000

IEC61850:GGIO:GGIO4:GGIO4.ANIN5

ANALOG IN 5 VALUE	SRC2 Ia Mag
ANALOG IN 5 DB	0.000 %
ANALOG IN 5 MIN	0.000
ANALOG IN 5 MAX	1000000.000

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

IEC61850:GGIO:GGIO4:GGIO4.ANIN6

ANALOG IN 6 VALUE	SRC2 Ib Mag
ANALOG IN 6 DB	0.000 %
ANALOG IN 6 MIN	0.000
ANALOG IN 6 MAX	1000000.000

IEC61850:GGIO:GGIO4:GGIO4.ANIN7

ANALOG IN 7 VALUE	SRC2 Ic Mag
ANALOG IN 7 DB	0.000 %
ANALOG IN 7 MIN	0.000
ANALOG IN 7 MAX	1000000.000

IEC61850:GGIO:GGIO4:GGIO4.ANIN8

ANALOG IN 8 VALUE	SRC2 In Mag
ANALOG IN 8 DB	0.000 %
ANALOG IN 8 MIN	0.000
ANALOG IN 8 MAX	1000000.000

HTTP

HTTP TCP Port Number	80

TFTP

TFTP Main UDP Port Number	69
TFTP Data UDP Port Number 1	0
TFTP Data UDP Port Number 2	0

IEC 60870-5-104

IEC TCP Port Number	2404
IEC Client Address 1	0. 0. 0. 0
IEC Client Address 2	0. 0. 0. 0
IEC Client Address 3	0. 0. 0. 0
IEC Client Address 4	0. 0. 0. 0
IEC Client Address 5	0. 0. 0. 0
IEC Common Address of ASDU	0
IEC Cyclic Data Period	60 s
IEC Current Default Threshold	30000
IEC Voltage Default Threshold	30000
IEC Power Default Threshold	30000
IEC Energy Default Threshold	30000
IEC Power Factor Default Threshold	1.00
IEC Other Default Threshold	30000
IEC Redundancy Enabled	No
IEC Event Time Base	LOCAL

REAL TIME CLOCK

GENERAL

Real Time Clock Events	Enabled

IRIG-B

IRIG-B Signal Type	DC Shift

LOCAL TIME

Time Zone Offset	0.0 hr

USER-PROGRAMMABLE FAULT REPORT

FAULT REPORT 1: Function	Enabled
FAULT REPORT 1: Prefault Trigger	OFF
FAULT REPORT 1: Fault Trigger	TRIPBUS 4 OP
FAULT REPORT 1: Analog Channel 1	Xfmr Iad Mag
FAULT REPORT 1: Analog Channel 2	Xfmr Ibd Mag
FAULT REPORT 1: Analog Channel 3	SRC1 Ia RMS
FAULT REPORT 1: Analog Channel 4	SRC1 Ib RMS
FAULT REPORT 1: Analog Channel 5	SRC1 Ic RMS
FAULT REPORT 1: Analog Channel 6	SRC1 Ig RMS
FAULT REPORT 1: Analog Channel 7	SRC2 Ia RMS
FAULT REPORT 1: Analog Channel 8	SRC2 Ib RMS
FAULT REPORT 1: Analog Channel 9	SRC2 Ic RMS
FAULT REPORT 1: Analog Channel 10	SRC2 Ig RMS
FAULT REPORT 1: Analog Channel 11	Xfmr Icd Mag
FAULT REPORT 1: Analog Channel 12	Xfmr Iar Mag
FAULT REPORT 1: Analog Channel 13	Xfmr Ibr Mag

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

USER-PROGRAMMABLE FAULT REPORT (continued from last page)

FAULT REPORT 1: Analog Channel 14	Xfmr Icr Mag
FAULT REPORT 1: Analog Channel 15	SRC1 Ia Mag
FAULT REPORT 1: Analog Channel 16	SRC1 Ib Mag
FAULT REPORT 1: Analog Channel 17	SRC1 Ic Mag
FAULT REPORT 1: Analog Channel 18	SRC1 Ig Mag
FAULT REPORT 1: Analog Channel 19	SRC1 Ia Angle
FAULT REPORT 1: Analog Channel 20	SRC1 Ib Angle
FAULT REPORT 1: Analog Channel 21	SRC1 Ic Angle
FAULT REPORT 1: Analog Channel 22	OFF
FAULT REPORT 1: Analog Channel 23	SRC2 Ia Mag
FAULT REPORT 1: Analog Channel 24	SRC2 Ib Mag
FAULT REPORT 1: Analog Channel 25	SRC2 Ic Mag
FAULT REPORT 1: Analog Channel 26	SRC2 Ia Angle
FAULT REPORT 1: Analog Channel 27	SRC2 Ib Angle
FAULT REPORT 1: Analog Channel 28	SRC2 Ic Angle
FAULT REPORT 1: Analog Channel 29	SRC2 Ig Mag
FAULT REPORT 1: Analog Channel 30	OFF
FAULT REPORT 1: Analog Channel 31	OFF
FAULT REPORT 1: Analog Channel 32	OFF

OSCILLOGRAPHY

Number Of Records	50
Trigger Mode	Automatic Overwrite
Trigger Position	60 %
Trigger Source	START OSC On (VO4)
AC Input Waveforms	64 samples/cycle
Digital Channel 1	BREAKER 1 OPEN
Digital Channel 2	BREAKER 1 Phase A CLOSED
Digital Channel 3	BREAKER 1 Phase B CLOSED
Digital Channel 4	BREAKER 1 Phase C CLOSED
Digital Channel 5	BREAKER 2 OPEN
Digital Channel 6	BREAKER 2 CLOSED
Digital Channel 7	TRIP_52JT2_B1_A Closed (U1)
Digital Channel 8	TRIP_52JT2_B1_B Closed (U2)
Digital Channel 9	TRIP_52JT2_B1_C Closed (U3)
Digital Channel 10	TRIP_52ET1_B1 Closed (W1)
Digital Channel 11	XFMR PCNT DIFF PKP A
Digital Channel 12	XFMR PCNT DIFF PKP B
Digital Channel 13	XFMR PCNT DIFF PKP C
Digital Channel 14	XFMR PCNT DIFF OP A
Digital Channel 15	XFMR PCNT DIFF OP B
Digital Channel 16	XFMR PCNT DIFF OP C
Digital Channel 17	XFMR PCNT DIFF 2ND A
Digital Channel 18	XFMR PCNT DIFF 2ND B
Digital Channel 19	XFMR PCNT DIFF 2ND C
Digital Channel 20	XFMR PCNT DIFF 5TH A
Digital Channel 21	XFMR PCNT DIFF 5TH B
Digital Channel 22	XFMR PCNT DIFF 5TH C
Digital Channel 23	XFMR INST DIFF OP A
Digital Channel 24	XFMR INST DIFF OP B
Digital Channel 25	XFMR INST DIFF OP C
Digital Channel 26	PHASE TOC1 PKP A
Digital Channel 27	PHASE TOC1 PKP B
Digital Channel 28	PHASE TOC1 PKP C
Digital Channel 29	PHASE TOC1 OP
Digital Channel 30	PHASE IOC1 PKP A
Digital Channel 31	PHASE IOC1 PKP B
Digital Channel 32	PHASE IOC1 PKP C
Digital Channel 33	PHASE IOC1 OP
Digital Channel 34	NEUTRAL TOC1 PKP
Digital Channel 35	NEUTRAL TOC1 OP
Digital Channel 36	PHASE TOC2 PKP A
Digital Channel 37	PHASE TOC2 PKP B
Digital Channel 38	PHASE TOC2 PKP C
Digital Channel 39	PHASE TOC2 OP
Digital Channel 40	PHASE IOC2 PKP A
Digital Channel 41	PHASE IOC2 PKP B
Digital Channel 42	PHASE IOC2 PKP C
Digital Channel 43	PHASE IOC2 OP
Digital Channel 44	NEUTRAL TOC2 PKP

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

OSCILLOGRAPHY (continued from last page)

Digital Channel 45	NEUTRAL TOC2 OP
Digital Channel 46	GROUND TOC1 PKP
Digital Channel 47	GROUND TOC1 OP
Digital Channel 48	86T2_OP On(H3c)
Digital Channel 49	86B3_OP On(P5a)
Digital Channel 50	ARRANQUE_50BF_52JT2 On(P6a)
Digital Channel 51	BKR FAIL 1 RETRIP
Digital Channel 52	BKR FAIL 1 TRIP OP
Digital Channel 53	86T2_OP On(H3c)
Digital Channel 54	86BF_JT2_OP On(P5c)
Digital Channel 55	BKR FAIL 2 RETRIP
Digital Channel 56	BKR FAIL 2 TRIP OP
Analog Channel 1	SRC1 Pa
Analog Channel 2	SRC1 Pb
Analog Channel 3	SRC1 Pc
Analog Channel 4	SRC1 P
Analog Channel 5	SRC1 Qa
Analog Channel 6	SRC1 Qb
Analog Channel 7	SRC1 Qc
Analog Channel 8	SRC1 Q
Analog Channel 9	SRC1 Frequency
Analog Channel 10	Xfmr Iad Mag
Analog Channel 11	Xfmr Ibd Mag
Analog Channel 12	Xfmr Icd Mag
Analog Channel 13	Xfmr Iar Mag
Analog Channel 14	Xfmr Ibr Mag
Analog Channel 15	Xfmr Icr Mag

DATA LOGGER

Data Logger Mode	Continuous
Data Logger Trigger	OFF
Rate	60000 msec
Configuration	0 CHNL x 0.0 DAYS

DEMAND

Current Method	Thermal Exponential
Power Method	Thermal Exponential
Interval	15 MIN
Trigger	OFF

USER-PROGRAMMABLE LEDS

LED TEST

Function	Enabled
Control	FLEXLOGIC ERR TOKEN

USER PROGRAMMABLE LEDS

LED 1: OPERAND	XFMR INST DIFF OP
LED 1: TYPE	Latched
LED 2: OPERAND	XFMR INST DIFF OP A
LED 2: TYPE	Latched
LED 3: OPERAND	XFMR INST DIFF OP B
LED 3: TYPE	Latched
LED 4: OPERAND	XFMR INST DIFF OP C
LED 4: TYPE	Latched
LED 5: OPERAND	XFMR PCNT DIFF OP
LED 5: TYPE	Latched
LED 6: OPERAND	XFMR PCNT DIFF OP A
LED 6: TYPE	Latched
LED 7: OPERAND	XFMR PCNT DIFF OP B
LED 7: TYPE	Latched
LED 8: OPERAND	XFMR PCNT DIFF OP C
LED 8: TYPE	Latched
LED 13: OPERAND	PHASE TOC1 OP
LED 13: TYPE	Latched
LED 14: OPERAND	PHASE IOC1 OP
LED 14: TYPE	Latched
LED 15: OPERAND	GROUND TOC1 OP
LED 15: TYPE	Latched
LED 16: OPERAND	GROUND IOC1 OP
LED 16: TYPE	Latched

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

USER PROGRAMMABLE LEDS (continued from last page)

LED 17: OPERAND	NEUTRAL TOC1 OP
LED 17: TYPE	Latched
LED 18: OPERAND	NEUTRAL IOC1 OP
LED 18: TYPE	Latched
LED 19: OPERAND	PHASE TOC2 OP
LED 19: TYPE	Latched
LED 20: OPERAND	PHASE IOC2 OP
LED 20: TYPE	Latched
LED 21: OPERAND	NEUTRAL TOC2 OP
LED 21: TYPE	Latched
LED 22: OPERAND	NEUTRAL IOC2 OP
LED 22: TYPE	Latched
LED 25: OPERAND	PHASE OV1 OP
LED 25: TYPE	Latched
LED 26: OPERAND	NEUTRAL OV1 OP
LED 26: TYPE	Latched
LED 27: OPERAND	PHASE UV1 OP
LED 27: TYPE	Latched
LED 30: OPERAND	BKR FAIL 1 T3 OP
LED 30: TYPE	Latched
LED 31: OPERAND	BKR FAIL 2 T3 OP
LED 31: TYPE	Latched
LED 33: OPERAND	TRIP 52JT2 On (VO1)
LED 33: TYPE	Latched
LED 34: OPERAND	TRIP 52ET2 On (VO2)
LED 34: TYPE	Latched
LED 37: OPERAND	86T2_OP On(H3c)
LED 37: TYPE	Self-Reset
LED 38: OPERAND	86BF_JT2_OP On(P5c)
LED 38: TYPE	Self-Reset
LED 39: OPERAND	86B3_OP On(P5a)
LED 39: TYPE	Self-Reset
LED 41: OPERAND	FALLA 52ET2 B1/B2 On(H4a)
LED 41: TYPE	Self-Reset
LED 42: OPERAND	ITM_TP/EB1 On(P6c)
LED 42: TYPE	Self-Reset
LED 48: OPERAND	IRIG-b FAILURE
LED 48: TYPE	Self-Reset

USER-PROGRAMMABLE SELF TESTS

Battery Fail Function	Enabled
IRIG B Fail Function	Enabled

USER-DEFINABLE DISPLAYS

USER DISPLAY 1: Top Line	Protecciyn Sist1 JT2
USER DISPLAY 1: Bottom Line	Minera Los Pelambres
USER DISPLAY 1: Item 1	0
USER DISPLAY 1: Item 2	0
USER DISPLAY 1: Item 3	0
USER DISPLAY 1: Item 4	0
USER DISPLAY 1: Item 5	0
USER DISPLAY 2: Top Line	TC 220kV Ia:~
USER DISPLAY 2: Bottom Line	Ib:~ Ic:~
USER DISPLAY 2: Item 1	282472
USER DISPLAY 2: Item 2	282475
USER DISPLAY 2: Item 3	282478
USER DISPLAY 2: Item 4	0
USER DISPLAY 2: Item 5	0
USER DISPLAY 3: Top Line	TC 23kV Ia:~
USER DISPLAY 3: Bottom Line	Ib:~ Ic:~
USER DISPLAY 3: Item 1	282536
USER DISPLAY 3: Item 2	282539
USER DISPLAY 3: Item 3	282542
USER DISPLAY 3: Item 4	0
USER DISPLAY 3: Item 5	0
USER DISPLAY 4: Top Line	TP 220kV Vab: ~
USER DISPLAY 4: Bottom Line	Vbc:~ Vca:~
USER DISPLAY 4: Item 1	268821
USER DISPLAY 4: Item 2	268824
USER DISPLAY 4: Item 3	268827

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

USER-DEFINABLE DISPLAYS (continued from last page)

USER DISPLAY 4: Item 4	0
USER DISPLAY 4: Item 5	0
USER DISPLAY 5: Top Line	TP 23kV Vab: ~
USER DISPLAY 5: Bottom Line	Vbc:~ Vca:~
USER DISPLAY 5: Item 1	268886
USER DISPLAY 5: Item 2	268888
USER DISPLAY 5: Item 3	268891
USER DISPLAY 5: Item 4	0
USER DISPLAY 5: Item 5	0
USER DISPLAY 6: Top Line	87T Idiff Ia:~
USER DISPLAY 6: Bottom Line	Ib:~ Ic:~
USER DISPLAY 6: Item 1	271105
USER DISPLAY 6: Item 2	271113
USER DISPLAY 6: Item 3	271121
USER DISPLAY 6: Item 4	0
USER DISPLAY 6: Item 5	0
USER DISPLAY 7: Top Line	87T IRest Ia:~
USER DISPLAY 7: Bottom Line	Ib:~ Ic:~
USER DISPLAY 7: Item 1	271107
USER DISPLAY 7: Item 2	271115
USER DISPLAY 7: Item 3	271123
USER DISPLAY 7: Item 4	0
USER DISPLAY 7: Item 5	0
USER DISPLAY 8: Top Line	Potencia Activa
USER DISPLAY 8: Bottom Line	3Ḱ: ~
USER DISPLAY 8: Item 1	269312
USER DISPLAY 8: Item 2	0
USER DISPLAY 8: Item 3	0
USER DISPLAY 8: Item 4	0
USER DISPLAY 8: Item 5	0
USER DISPLAY 9: Top Line	Potencia Reactiva
USER DISPLAY 9: Bottom Line	3Ḱ: ~
USER DISPLAY 9: Item 1	269320
USER DISPLAY 9: Item 2	0
USER DISPLAY 9: Item 3	0
USER DISPLAY 9: Item 4	0
USER DISPLAY 9: Item 5	0
Invoke and Scroll	OFF

INSTALLATION

Relay Settings	Programmed
Relay Name	AA1D1Q01FN1

RESERVED

Spare Setting 1	Disabled
Spare Setting 2	Disabled
Spare Setting 3	0
Spare Setting 4	0
Spare Setting 5	Disabled
Spare Setting 6	Disabled
Spare Setting 7	Disabled
Spare Setting 8	Disabled
Spare Setting 9	0
Spare Setting 10	0
Spare Setting 11	0
Spare Setting 12	0

SYSTEM SETUP

AC INPUTS

CURRENT

CT F1: Phase CT Primary	850 A
CT F1: Phase CT Secondary	5 A
CT F1: Ground CT Primary	850 A
CT F1: Ground CT Secondary	5 A
CT M1: Phase CT Primary	400 A
CT M1: Phase CT Secondary	5 A
CT M1: Ground CT Primary	75 A
CT M1: Ground CT Secondary	5 A

VOLTAGE

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

VOLTAGE (continued from last page)

VT F5: Phase VT Connection	Wye
VT F5: Phase VT Secondary	66.9 V
VT F5: Phase VT Ratio	2000.00 :1
VT F5: Auxiliary VT Connection	Vag
VT F5: Auxiliary VT Secondary	66.4 V
VT F5: Auxiliary VT Ratio	2000.00 :1
VT M5: Phase VT Connection	Wye
VT M5: Phase VT Secondary	40.0 V
VT M5: Phase VT Ratio	361.27 :1
VT M5: Auxiliary VT Connection	Vag
VT M5: Auxiliary VT Secondary	40.0 V
VT M5: Auxiliary VT Ratio	361.27 :1

POWER SYSTEM

Nominal Frequency	50 Hz
Phase Rotation	ABC
Frequency And Phase Reference	JT2 (SRC 1)
Frequency Tracking Function	Enabled

SIGNAL SOURCES

SOURCE 1: Name	JT2
SOURCE 1: Phase CT	M1
SOURCE 1: Ground CT	None
SOURCE 1: Phase VT	F5
SOURCE 1: Auxiliary VT	None
SOURCE 1: AB Loop Impedance Angle	None
SOURCE 1: Alt Phase CT	None
SOURCE 1: Alt Ground CT	None
SOURCE 1: Switch Alt CT	OFF
SOURCE 1: AB Loop Impedance Angle	None
SOURCE 1: Alt Phase VT	None
SOURCE 1: Alt Auxiliary VT	None
SOURCE 1: Switch Alt VT	OFF
SOURCE 2: Name	ET2
SOURCE 2: Phase CT	F1
SOURCE 2: Ground CT	M1
SOURCE 2: Phase VT	M5
SOURCE 2: Auxiliary VT	None
SOURCE 2: AB Loop Impedance Angle	None
SOURCE 2: Alt Phase CT	None
SOURCE 2: Alt Ground CT	None
SOURCE 2: Switch Alt CT	OFF
SOURCE 2: AB Loop Impedance Angle	None
SOURCE 2: Alt Phase VT	None
SOURCE 2: Alt Auxiliary VT	None
SOURCE 2: Switch Alt VT	OFF

TRANSFORMER

GENERAL

Number Of Windings	2
Reference Winding Selection	Winding 2
Phase Compensation	Internal (software)
Load Loss At Rated Load	412 kW
Rated Winding Temperature Rise	65°C (oil)
No Load Loss	45 kW
Type Of Cooling	FA
Top-oil Rise Over Ambient	60 °C
Thermal Capacity	100.00 kWh/°C
Winding Thermal Time Constant	2.00 min

WINDINGS

WINDING 1: Source	JT2 (SRC 1)
WINDING 1: Status	ON
WINDING 1: Rated MVA	30.000 MVA
WINDING 1: Nominal Phs-phs Voltage	220.000 kV
WINDING 1: Connection	Delta
WINDING 1: Grounding	Not within zone
WINDING 1: Angle Wrt Winding 1	0.0 deg
WINDING 1: Resistance	0.0829 ohms
WINDING 2: Source	ET2 (SRC 2)

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

WINDINGS (continued from last page)

WINDING 2: Status	ON
WINDING 2: Rated MVA	30.000 MVA
WINDING 2: Nominal Phs-phs Voltage	23.000 kV
WINDING 2: Connection	Wye
WINDING 2: Grounding	Within zone
WINDING 2: Angle Wrt Winding 1	-30.0 deg
WINDING 2: Resistance	0.0417 ohms

THERMAL INPUTS

Winding Currents	JT2 (SRC 1)
Ambient Temperature Sensor	RRTD Input 1
Ambient Temperature January Average	-20 °C
Ambient Temperature February Average	-30 °C
Ambient Temperature March Average	-10 °C
Ambient Temperature April Average	10 °C
Ambient Temperature May Average	20 °C
Ambient Temperature June Average	30 °C
Ambient Temperature July Average	30 °C
Ambient Temperature August Average	30 °C
Ambient Temperature September Average	20 °C
Ambient Temperature October Average	10 °C
Ambient Temperature November Average	10 °C
Ambient Temperature December Average	-10 °C
Top Oil Temperature Sensor	RRTD Input 1

BREAKERS

Breaker 1 Function	Enabled
Breaker 1 Name	52JT2
Breaker 1 Mode	1-Pole
Breaker 1 Phase A/3-Pole Open	52JT2_AB On(H2c)
Breaker 1 Phase A/3-Pole Close	52JT2_CE_A On(H1a)
Breaker 1 Phase B Open	52JT2_AB On(H2c)
Breaker 1 Phase B Close	52JT2_CE_B On(H1c)
Breaker 1 Phase C Open	52JT2_AB On(H2c)
Breaker 1 Phase C Close	52JT2_CE_C On(H2a)
Breaker 1 Alarm Delay	0.000 s
Breaker 1 External Alarm	OFF
Breaker 1 Racked In	OFF
Breaker 1 Out Of Service	OFF
Breaker 1 Open	OFF
Breaker 1 Close	OFF
Breaker 1 Push Button Control	OFF
Breaker 1 Toperate	70 ms
Breaker 1 Manual Close Recall Time	0.000 s
Breaker 1 Open Seal In	0.000 s
Breaker 1 Block Open	OFF
Breaker 1 Block Close	OFF
Breaker 1 Bypass	Disabled
Breaker 1 Tagging	Disabled
Breaker 1 Substitution	Disabled
Breaker 1 Events	Disabled
Breaker 1 Local	OFF
Breaker 1 Interlock Open	ON
Breaker 1 Interlock Close	ON
Breaker 1 Position Reached Check	Enabled
Breaker 1 Synccheck Close	ON
Breaker 2 Function	Enabled
Breaker 2 Name	52ET1
Breaker 2 Mode	3-Pole
Breaker 2 Phase A/3-Pole Open	52ET2_AB On(H8c)
Breaker 2 Phase A/3-Pole Close	52ET2_CE On(H8a)
Breaker 2 Phase B Open	OFF
Breaker 2 Phase B Close	OFF
Breaker 2 Phase C Open	OFF
Breaker 2 Phase C Close	OFF
Breaker 2 Alarm Delay	0.000 s
Breaker 2 External Alarm	OFF
Breaker 2 Racked In	OFF
Breaker 2 Out Of Service	OFF
Breaker 2 Open	OFF

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

BREAKERS (continued from last page)

Breaker 2 Close	OFF
Breaker 2 Push Button Control	OFF
Breaker 2 Toperate	70 ms
Breaker 2 Manual Close Recall Time	0.000 s
Breaker 2 Open Seal In	0.000 s
Breaker 2 Block Open	OFF
Breaker 2 Block Close	OFF
Breaker 2 Bypass	Disabled
Breaker 2 Tagging	Disabled
Breaker 2 Substitution	Disabled
Breaker 2 Events	Disabled
Breaker 2 Local	OFF
Breaker 2 Interlock Open	ON
Breaker 2 Interlock Close	ON
Breaker 2 Position Reached Check	Enabled
Breaker 2 Synccheck Close	ON

FLEXLOGIC

FLEXLOGIC EQUATION EDITOR

FlexLogic Entry 1	TRIPBUS 1 OP
FlexLogic Entry 2	= TRIP 52JT2 (VO1)
FlexLogic Entry 3	TRIPBUS 2 OP
FlexLogic Entry 4	= TRIP 52ET2 (VO2)
FlexLogic Entry 5	TRIPBUS 3 OP
FlexLogic Entry 6	= TRIP 86T (VO3)
FlexLogic Entry 7	XFMR PCNT DIFF PKP A
FlexLogic Entry 8	XFMR PCNT DIFF PKP B
FlexLogic Entry 9	XFMR PCNT DIFF PKP C
FlexLogic Entry 10	PHASE TOC1 PKP
FlexLogic Entry 11	PHASE TOC2 PKP
FlexLogic Entry 12	PHASE IOC1 PKP
FlexLogic Entry 13	PHASE IOC2 PKP
FlexLogic Entry 14	NEUTRAL TOC1 PKP
FlexLogic Entry 15	NEUTRAL TOC2 PKP
FlexLogic Entry 16	GROUND TOC1 PKP
FlexLogic Entry 17	OR(10)
FlexLogic Entry 18	= START GENERAL (VO9)
FlexLogic Entry 19	TRIPBUS 1 OP
FlexLogic Entry 20	TRIPBUS 2 OP
FlexLogic Entry 21	TRIPBUS 3 OP
FlexLogic Entry 22	TRIPBUS 4 OP
FlexLogic Entry 23	OR(4)
FlexLogic Entry 24	= RESUMEN TRIP (VO11)
FlexLogic Entry 25	86T2_OP On(H3c)
FlexLogic Entry 26	86B3_OP On(P5a)
FlexLogic Entry 27	ARRANQUE_50BF_52JT2 On(P6a)
FlexLogic Entry 28	TRIPBUS 1 OP
FlexLogic Entry 29	OR(4)
FlexLogic Entry 30	= ARQ 50BF 52JT2 (VO13)
FlexLogic Entry 31	86T2_OP On(H3c)
FlexLogic Entry 32	86BF_JT2_OP On(P5c)
FlexLogic Entry 33	TRIPBUS 2 OP
FlexLogic Entry 34	OR(3)
FlexLogic Entry 35	= ARQ 50BF 52ET2 (VO14)
FlexLogic Entry 36	TRIPBUS 1 OP
FlexLogic Entry 37	POSITIVE ONE SHOT
FlexLogic Entry 38	TRIPBUS 2 OP
FlexLogic Entry 39	POSITIVE ONE SHOT
FlexLogic Entry 40	TRIPBUS 3 OP
FlexLogic Entry 41	POSITIVE ONE SHOT
FlexLogic Entry 42	OR(3)
FlexLogic Entry 43	BREAKER 1 OPEN
FlexLogic Entry 44	POSITIVE ONE SHOT
FlexLogic Entry 45	BREAKER 1 CLOSED
FlexLogic Entry 46	POSITIVE ONE SHOT
FlexLogic Entry 47	OR(2)
FlexLogic Entry 48	BREAKER 2 OPEN
FlexLogic Entry 49	POSITIVE ONE SHOT
FlexLogic Entry 50	BREAKER 2 CLOSED
FlexLogic Entry 51	POSITIVE ONE SHOT

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

FLEXLOGIC EQUATION EDITOR (continued from last page)

FlexLogic Entry 52	OR(2)
FlexLogic Entry 53	START GENERA On (VO5)
FlexLogic Entry 54	POSITIVE ONE SHOT
FlexLogic Entry 55	XFMR PCNT DIFF 2ND A
FlexLogic Entry 56	POSITIVE ONE SHOT
FlexLogic Entry 57	XFMR PCNT DIFF 2ND B
FlexLogic Entry 58	POSITIVE ONE SHOT
FlexLogic Entry 59	XFMR PCNT DIFF 2ND C
FlexLogic Entry 60	POSITIVE ONE SHOT
FlexLogic Entry 61	OR(3)
FlexLogic Entry 62	XFMR PCNT DIFF 5TH A
FlexLogic Entry 63	POSITIVE ONE SHOT
FlexLogic Entry 64	XFMR PCNT DIFF 5TH B
FlexLogic Entry 65	POSITIVE ONE SHOT
FlexLogic Entry 66	XFMR PCNT DIFF 5TH C
FlexLogic Entry 67	POSITIVE ONE SHOT
FlexLogic Entry 68	OR(3)
FlexLogic Entry 69	86T2_OP On(H3c)
FlexLogic Entry 70	POSITIVE ONE SHOT
FlexLogic Entry 71	86B3_OP On(P5a)
FlexLogic Entry 72	POSITIVE ONE SHOT
FlexLogic Entry 73	ARRANQUE_50BF_52JT2 On(P6a)
FlexLogic Entry 74	POSITIVE ONE SHOT
FlexLogic Entry 75	86BF_JT2_OP On(P5c)
FlexLogic Entry 76	POSITIVE ONE SHOT
FlexLogic Entry 77	OR(4)
FlexLogic Entry 78	BKR FAIL 1 RETRIP
FlexLogic Entry 79	POSITIVE ONE SHOT
FlexLogic Entry 80	BKR FAIL 1 TRIP OP
FlexLogic Entry 81	POSITIVE ONE SHOT
FlexLogic Entry 82	BKR FAIL 2 RETRIP
FlexLogic Entry 83	POSITIVE ONE SHOT
FlexLogic Entry 84	BKR FAIL 2 TRIP OP
FlexLogic Entry 85	POSITIVE ONE SHOT
FlexLogic Entry 86	OR(4)
FlexLogic Entry 87	OR(8)
FlexLogic Entry 88	TIMER 1
FlexLogic Entry 89	= START OSC (VO4)
FlexLogic Entry 90	XFMR PCNT DIFF 2ND A
FlexLogic Entry 91	= XFMR PCNT DIFF 2ND A (VO30)
FlexLogic Entry 92	XFMR PCNT DIFF 2ND B
FlexLogic Entry 93	= XFMR PCNT DIFF 2ND B (VO31)
FlexLogic Entry 94	XFMR PCNT DIFF 2ND C
FlexLogic Entry 95	= XFMR PCNT DIFF 2ND C (VO32)
FlexLogic Entry 96	XFMR PCNT DIFF 5TH A
FlexLogic Entry 97	= XFMR PCNT DIFF 5TH A (VO33)
FlexLogic Entry 98	XFMR PCNT DIFF 5TH B
FlexLogic Entry 99	= XFMR PCNT DIFF 5TH B (VO34)
FlexLogic Entry 100	XFMR PCNT DIFF 5TH C
FlexLogic Entry 101	= XFMR PCNT DIFF 5TH C (VO35)
FlexLogic Entry 102	XFMR PCNT DIFF PKP A
FlexLogic Entry 103	XFMR PCNT DIFF PKP B
FlexLogic Entry 104	XFMR PCNT DIFF PKP C
FlexLogic Entry 105	OR(3)
FlexLogic Entry 106	= Virt Op 128 (VO128)
FlexLogic Entry 107	HARMONIC DET 1 PKP
FlexLogic Entry 108	HARMONIC DET 2 PKP
FlexLogic Entry 109	OR(2)
FlexLogic Entry 110	= Virt Op 127 (VO127)
FlexLogic Entry 111	LED PICKUP
FlexLogic Entry 112	= START GENERA (VO5)
FlexLogic Entry 113	END

FLEXLOGIC TIMERS

Timer 1: Type	millisecond
Timer 1: Pickup Delay	0
Timer 1: Dropout Delay	600

GROUPED ELEMENTS
GROUP 1

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

TRANSFORMER

PERCENT DIFFERENTIAL [GROUP 1]

Function	Enabled
Pickup	0.300 pu
Slope 1	40 %
Break 1	2.000 pu
Break 2	8.000 pu
Slope 2	80 %
Inrush Inhibit Function	Adapt. 2nd
Inrush Inhibit Mode	Per phase
Inrush Inhibit Level	20.0 % fo
Overexcitation Inhibit Function	5th
Overexcitation Inhibit Level	35.0 % fo
Block	OFF
Target	Latched
Events	Enabled

INSTANTANEOUS DIFFERENTIAL [GROUP 1]

Function	Enabled
Pickup	10.000 pu
Block	OFF
Target	Latched
Events	Enabled

PHASE CURRENT

PHASE TOC [GROUP 1]

PHASE TOC1: Function	Enabled
PHASE TOC1: Signal Source	JT2 (SRC 1)
PHASE TOC1: Input	Phasor
PHASE TOC1: Pickup	0.236 pu
PHASE TOC1: Curve	IEEE Mod Inv
PHASE TOC1: TD Multiplier	0.40
PHASE TOC1: Reset	Instantaneous
PHASE TOC1: Voltage Restraint	Disabled
PHASE TOC1: Block A	OFF
PHASE TOC1: Block B	OFF
PHASE TOC1: Block C	OFF
PHASE TOC1: Target	Latched
PHASE TOC1: Events	Enabled
PHASE TOC2: Function	Enabled
PHASE TOC2: Signal Source	ET2 (SRC 2)
PHASE TOC2: Input	Phasor
PHASE TOC2: Pickup	1.063 pu
PHASE TOC2: Curve	IEEE Mod Inv
PHASE TOC2: TD Multiplier	0.35
PHASE TOC2: Reset	Instantaneous
PHASE TOC2: Voltage Restraint	Disabled
PHASE TOC2: Block A	OFF
PHASE TOC2: Block B	OFF
PHASE TOC2: Block C	OFF
PHASE TOC2: Target	Latched
PHASE TOC2: Events	Enabled

PHASE IOC [GROUP 1]

PHASE IOC1: Function	Enabled
PHASE IOC1: Source	JT2 (SRC 1)
PHASE IOC1: Pickup	2.082 pu
PHASE IOC1: Delay	0.10 s
PHASE IOC1: Reset Delay	0.00 s
PHASE IOC1: Block A	OFF
PHASE IOC1: Block B	OFF
PHASE IOC1: Block C	OFF
PHASE IOC1: Target	Latched
PHASE IOC1: Events	Enabled
PHASE IOC2: Function	Enabled
PHASE IOC2: Source	ET2 (SRC 2)
PHASE IOC2: Pickup	4.449 pu
PHASE IOC2: Delay	0.40 s
PHASE IOC2: Reset Delay	0.00 s
PHASE IOC2: Block A	OFF
PHASE IOC2: Block B	OFF

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

PHASE IOC [GROUP 1] (continued from last page)

PHASE IOC2: Block C	OFF
PHASE IOC2: Target	Latched
PHASE IOC2: Events	Enabled

NEUTRAL CURRENT

NEUTRAL TOC [GROUP 1]

NEUTRAL TOC1: Function	Enabled
NEUTRAL TOC1: Source	JT2 (SRC 1)
NEUTRAL TOC1: Input	Phasor
NEUTRAL TOC1: Pickup	0.100 pu
NEUTRAL TOC1: Curve	Definite Time
NEUTRAL TOC1: TD Multiplier	0.00
NEUTRAL TOC1: Reset	Instantaneous
NEUTRAL TOC1: Block	OFF
NEUTRAL TOC1: Target	Latched
NEUTRAL TOC1: Events	Enabled
NEUTRAL TOC2: Function	Enabled
NEUTRAL TOC2: Source	ET2 (SRC 2)
NEUTRAL TOC2: Input	Phasor
NEUTRAL TOC2: Pickup	0.100 pu
NEUTRAL TOC2: Curve	Definite Time
NEUTRAL TOC2: TD Multiplier	0.40
NEUTRAL TOC2: Reset	Instantaneous
NEUTRAL TOC2: Block	OFF
NEUTRAL TOC2: Target	Latched
NEUTRAL TOC2: Events	Enabled

GROUND CURRENT

GROUND TOC [GROUP 1]

GROUND TOC1: Function	Enabled
GROUND TOC1: Source	ET2 (SRC 2)
GROUND TOC1: Input	Phasor
GROUND TOC1: Pickup	0.100 pu
GROUND TOC1: Curve	Definite Time
GROUND TOC1: TD Multiplier	0.40
GROUND TOC1: Reset	Instantaneous
GROUND TOC1: Block	OFF
GROUND TOC1: Target	Latched
GROUND TOC1: Events	Enabled

BREAKER FAILURE [GROUP 1]

BF 1: Function	Enabled
BF 1: Mode	3-Pole
BF 1: Source	JT2 (SRC 1)
BF 1: Current Supervision	Yes
BF 1: Use Seal-In	Yes
BF 1: 3-Pole Initiate	ARQ 50BF 52JT2 On (VO13)
BF 1: Block	52JT2_AB On(H2c)
BF 1: Retrip Pickup Delay	0.010 s
BF 1: Phase Current Supv Pickup	1.200 pu
BF 1: Neutral Current Supv Pickup	0.100 pu
BF 1: Use Timer 1	Yes
BF 1: Timer 1 Pickup Delay	0.200 s
BF 1: Use Timer 2	Yes
BF 1: Timer 2 Pickup Delay	0.200 s
BF 1: Use Timer 3	Yes
BF 1: Timer 3 Pickup Delay	0.200 s
BF 1: BKR Pos1 Ph A/3P	BREAKER 1 Phase A CLOSED
BF 1: BKR Pos2 Ph A/3P	OFF
BF 1: Breaker Test On	OFF
BF 1: Phase Current HiSet Pickup	1.200 pu
BF 1: Neutral Current HiSet Pickup	0.100 pu
BF 1: Phase Current LoSet Pickup	1.200 pu
BF 1: Neutral Current LoSet Pickup	0.100 pu
BF 1: LoSet Time Delay	0.000 s
BF 1: Trip Dropout Delay	0.100 s
BF 1: Target	Self-reset
BF 1: Events	Enabled
BF 1: Ph A Initiate	OFF
BF 1: Ph B Initiate	OFF

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

BREAKER FAILURE [GROUP 1] (continued from last page)

BF 1: Ph C Initiate	OFF
BF 1: BKR Pos1 Ph B	BREAKER 1 Phase B CLOSED
BF 1: BKR Pos1 Ph C	BREAKER 1 Phase C CLOSED
BF 1: BKR Pos2 Ph B	OFF
BF 1: BKR Pos2 Ph C	OFF
BF 2: Function	Enabled
BF 2: Mode	3-Pole
BF 2: Source	ET2 (SRC 2)
BF 2: Current Supervision	Yes
BF 2: Use Seal-In	Yes
BF 2: 3-Pole Initiate	ARQ 50BF 52ET2 On (VO14)
BF 2: Block	52ET2_AB On(H8c)
BF 2: Retrip Pickup Delay	0.010 s
BF 2: Phase Current Supv Pickup	1.200 pu
BF 2: Neutral Current Supv Pickup	0.100 pu
BF 2: Use Timer 1	Yes
BF 2: Timer 1 Pickup Delay	0.200 s
BF 2: Use Timer 2	Yes
BF 2: Timer 2 Pickup Delay	0.200 s
BF 2: Use Timer 3	Yes
BF 2: Timer 3 Pickup Delay	0.200 s
BF 2: BKR Pos1 Ph A/3P	BREAKER 2 CLOSED
BF 2: BKR Pos2 Ph A/3P	OFF
BF 2: Breaker Test On	OFF
BF 2: Phase Current HiSet Pickup	1.200 pu
BF 2: Neutral Current HiSet Pickup	0.100 pu
BF 2: Phase Current LoSet Pickup	1.200 pu
BF 2: Neutral Current LoSet Pickup	0.100 pu
BF 2: LoSet Time Delay	0.000 s
BF 2: Trip Dropout Delay	0.100 s
BF 2: Target	Self-reset
BF 2: Events	Enabled
BF 2: Ph A Initiate	OFF
BF 2: Ph B Initiate	OFF
BF 2: Ph C Initiate	OFF
BF 2: BKR Pos1 Ph B	OFF
BF 2: BKR Pos1 Ph C	OFF
BF 2: BKR Pos2 Ph B	OFF
BF 2: BKR Pos2 Ph C	OFF

CONTROL ELEMENTS

TRIP BUS

Trip Bus 1 Function	Enabled
Trip Bus 1 Block	OFF
Trip Bus 1 Pickup Delay	0.00 s
Trip Bus 1 Reset Delay	3.00 s
Trip Bus 1 Input 1	XFMR INST DIFF OP
Trip Bus 1 Input 2	XFMR PCNT DIFF OP
Trip Bus 1 Input 3	PHASE TOC1 OP
Trip Bus 1 Input 4	PHASE IOC1 OP
Trip Bus 1 Input 5	NEUTRAL TOC1 OP
Trip Bus 1 Input 6	GROUND TOC1 OP
Trip Bus 1 Input 7	BKR FAIL 1 RETRIP
Trip Bus 1 Input 8	BKR FAIL 2 TRIP OP
Trip Bus 1 Input 9	OFF
Trip Bus 1 Input 10	OFF
Trip Bus 1 Input 11	OFF
Trip Bus 1 Input 12	OFF
Trip Bus 1 Input 13	OFF
Trip Bus 1 Input 14	OFF
Trip Bus 1 Input 15	OFF
Trip Bus 1 Input 16	OFF
Trip Bus 1 Latching	Disabled
Trip Bus 1 Reset	OFF
Trip Bus 1 Target	Self-reset
Trip Bus 1 Events	Enabled
Trip Bus 2 Function	Enabled
Trip Bus 2 Block	OFF
Trip Bus 2 Pickup Delay	0.00 s
Trip Bus 2 Reset Delay	3.00 s

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

TRIP BUS (continued from last page)

Trip Bus 2 Input 1	XFMR INST DIFF OP
Trip Bus 2 Input 2	XFMR PCNT DIFF OP
Trip Bus 2 Input 3	PHASE TOC2 OP
Trip Bus 2 Input 4	PHASE IOC2 OP
Trip Bus 2 Input 5	NEUTRAL TOC2 OP
Trip Bus 2 Input 6	GROUND TOC1 OP
Trip Bus 2 Input 7	BKR FAIL 2 RETRIP
Trip Bus 2 Input 8	BKR FAIL 1 TRIP OP
Trip Bus 2 Input 9	OFF
Trip Bus 2 Input 10	OFF
Trip Bus 2 Input 11	OFF
Trip Bus 2 Input 12	OFF
Trip Bus 2 Input 13	OFF
Trip Bus 2 Input 14	OFF
Trip Bus 2 Input 15	OFF
Trip Bus 2 Input 16	OFF
Trip Bus 2 Latching	Disabled
Trip Bus 2 Reset	OFF
Trip Bus 2 Target	Self-reset
Trip Bus 2 Events	Enabled
Trip Bus 3 Function	Enabled
Trip Bus 3 Block	OFF
Trip Bus 3 Pickup Delay	0.00 s
Trip Bus 3 Reset Delay	3.00 s
Trip Bus 3 Input 1	XFMR INST DIFF OP
Trip Bus 3 Input 2	XFMR PCNT DIFF OP
Trip Bus 3 Input 3	PHASE TOC1 OP
Trip Bus 3 Input 4	PHASE IOC1 OP
Trip Bus 3 Input 5	NEUTRAL TOC1 OP
Trip Bus 3 Input 6	GROUND TOC1 OP
Trip Bus 3 Input 7	OFF
Trip Bus 3 Input 8	OFF
Trip Bus 3 Input 9	OFF
Trip Bus 3 Input 10	OFF
Trip Bus 3 Input 11	OFF
Trip Bus 3 Input 12	OFF
Trip Bus 3 Input 13	OFF
Trip Bus 3 Input 14	OFF
Trip Bus 3 Input 15	OFF
Trip Bus 3 Input 16	OFF
Trip Bus 3 Latching	Disabled
Trip Bus 3 Reset	OFF
Trip Bus 3 Target	Self-reset
Trip Bus 3 Events	Enabled

INPUTS/OUTPUTS

CONTACT INPUTS

[H1a] Contact Input 1a ID	52JT2_CE_A
[H1a] Contact Input 1a Debounce Time	2.0 ms
[H1a] Contact Input 1a Events	Enabled
[H1c] Contact Input 1c ID	52JT2_CE_B
[H1c] Contact Input 1c Debounce Time	2.0 ms
[H1c] Contact Input 1c Events	Enabled
[H2a] Contact Input 2a ID	52JT2_CE_C
[H2a] Contact Input 2a Debounce Time	2.0 ms
[H2a] Contact Input 2a Events	Enabled
[H2c] Contact Input 2c ID	52JT2_AB
[H2c] Contact Input 2c Debounce Time	2.0 ms
[H2c] Contact Input 2c Events	Enabled
[H3a] Contact Input 3a ID	PS1_PRUEBA
[H3a] Contact Input 3a Debounce Time	2.0 ms
[H3a] Contact Input 3a Events	Enabled
[H3c] Contact Input 3c ID	86T2_OP
[H3c] Contact Input 3c Debounce Time	2.0 ms
[H3c] Contact Input 3c Events	Enabled
[H4a] Contact Input 4a ID	FALLA 52ET2 B1/B2
[H4a] Contact Input 4a Debounce Time	2.0 ms
[H4a] Contact Input 4a Events	Disabled
[H4c] Contact Input 4c ID	FALLA_PS2/JT2
[H4c] Contact Input 4c Debounce Time	2.0 ms

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

CONTACT INPUTS (continued from last page)

[H4c] Contact Input 4c Events	Enabled
[H5a] Contact Input 5a ID	SUP_BIN_PS2/JT2
[H5a] Contact Input 5a Debounce Time	2.0 ms
[H5a] Contact Input 5a Events	Enabled
[H5c] Contact Input 5c ID	SUP_BIN_BC1/JT2
[H5c] Contact Input 5c Debounce Time	1.5 ms
[H5c] Contact Input 5c Events	Enabled
[H6a] Contact Input 6a ID	SUP_BIN_PS1/JT2
[H6a] Contact Input 6a Debounce Time	2.0 ms
[H6a] Contact Input 6a Events	Enabled
[H6c] Contact Input 6c ID	RESERVA
[H6c] Contact Input 6c Debounce Time	2.0 ms
[H6c] Contact Input 6c Events	Disabled
[H7a] Contact Input 7a ID	FALLA_BC1/JT2
[H7a] Contact Input 7a Debounce Time	2.0 ms
[H7a] Contact Input 7a Events	Enabled
[H7c] Contact Input 7c ID	SUP_RELE_AUX
[H7c] Contact Input 7c Debounce Time	2.0 ms
[H7c] Contact Input 7c Events	Enabled
[H8a] Contact Input 8a ID	52ET2_CE
[H8a] Contact Input 8a Debounce Time	2.0 ms
[H8a] Contact Input 8a Events	Enabled
[H8c] Contact Input 8c ID	52ET2_AB
[H8c] Contact Input 8c Debounce Time	2.0 ms
[H8c] Contact Input 8c Events	Enabled
[P5a] Contact Input 5a ID	86B3_OP
[P5a] Contact Input 5a Debounce Time	6.0 ms
[P5a] Contact Input 5a Events	Enabled
[P5c] Contact Input 5c ID	86BF_JT2_OP
[P5c] Contact Input 5c Debounce Time	6.0 ms
[P5c] Contact Input 5c Events	Enabled
[P6a] Contact Input 6a ID	ARRANQUE_50BF_52JT2
[P6a] Contact Input 6a Debounce Time	6.0 ms
[P6a] Contact Input 6a Events	Enabled
[P6c] Contact Input 6c ID	ITM_TP/EB1
[P6c] Contact Input 6c Debounce Time	6.0 ms
[P6c] Contact Input 6c Events	Enabled
[P7a] Contact Input 7a ID	T° Aceite
[P7a] Contact Input 7a Debounce Time	6.0 ms
[P7a] Contact Input 7a Events	Enabled
[P7c] Contact Input 7c ID	Detector Gas
[P7c] Contact Input 7c Debounce Time	6.0 ms
[P7c] Contact Input 7c Events	Enabled
[P8a] Contact Input 8a ID	Aceite Max
[P8a] Contact Input 8a Debounce Time	6.0 ms
[P8a] Contact Input 8a Events	Enabled
[P8c] Contact Input 8c ID	Aceite Min
[P8c] Contact Input 8c Debounce Time	6.0 ms
[P8c] Contact Input 8c Events	Enabled

CONTACT INPUT THRESHOLDS

52JT2_CE_A 52JT2_CE_B 52JT2_CE_C 52JT2_A B(H1a H1c H2a H2c)	84 Vdc
PS1_PRUEBA 86T2_OP FALLA 52ET2 B1/B2 FAL LA_PS2/JT2(H3a H3c H4a H4c)	84 Vdc
SUP_BIN_PS2/JT2 SUP_BIN_BC1/JT2 SUP_BIN_ PS1/JT2 RESERVA(H5a H5c H6a H6c)	84 Vdc
FALLA_BC1/JT2 SUP_RELE_AUX 52ET2_CE 52ET 2_AB(H7a H7c H8a H8c)	84 Vdc
86B3_OP 86BF_JT2_OP ARRANQUE_50BF_52JT2 ITM_TP/EB1(P5a P5c P6a P6c)	84 Vdc
T° Aceite Detector Gas Aceite Max Aceite Min(P7a P7c P8a P8c)	84 Vdc

VIRTUAL INPUTS

Virtual Input 1 Function	Enabled
Virtual Input 1 ID	RESET LEDS
Virtual Input 1 Type	Self-Reset
Virtual Input 1 Events	Disabled

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

CONTACT OUTPUTS

[P1] Contact Output 1 ID	ARRANQUE_50BF_52JT2
[P1] Contact Output 1 Operate	TRIPBUS 1 OP
[P1] Contact Output 1 Seal-In	OFF
[P1] Contact Output 1 Events	Enabled
[P2] Contact Output 2 ID	TRIP_50BF_87B
[P2] Contact Output 2 Operate	BKR FAIL 1 TRIP OP
[P2] Contact Output 2 Seal-In	OFF
[P2] Contact Output 2 Events	Enabled
[P3] Contact Output 3 ID	TRIP_86BF_JT2
[P3] Contact Output 3 Operate	BKR FAIL 1 TRIP OP
[P3] Contact Output 3 Seal-In	OFF
[P3] Contact Output 3 Events	Enabled
[U1] Contact Output 5 ID	TRIP_52JT2_B1_A
[U1] Contact Output 5 Operate	TRIP 52JT2 On (VO1)
[U1] Contact Output 5 Seal-In	OFF
[U1] Contact Output 5 Events	Enabled
[U2] Contact Output 6 ID	TRIP_52JT2_B1_B
[U2] Contact Output 6 Operate	TRIP 52JT2 On (VO1)
[U2] Contact Output 6 Seal-In	OFF
[U2] Contact Output 6 Events	Enabled
[U3] Contact Output 7 ID	TRIP_52JT2_B1_C
[U3] Contact Output 7 Operate	TRIP 52JT2 On (VO1)
[U3] Contact Output 7 Seal-In	OFF
[U3] Contact Output 7 Events	Enabled
[U4] Contact Output 8 ID	TRIP_52JT2_B2_A
[U4] Contact Output 8 Operate	TRIP 52JT2 On (VO1)
[U4] Contact Output 8 Seal-In	OFF
[U4] Contact Output 8 Events	Enabled
[U5] Contact Output 9 ID	TRIP_52JT2_B2_B
[U5] Contact Output 9 Operate	TRIP 52JT2 On (VO1)
[U5] Contact Output 9 Seal-In	OFF
[U5] Contact Output 9 Events	Enabled
[U6] Contact Output 10 ID	TRIP_52JT2_B2_C
[U6] Contact Output 10 Operate	TRIP 52JT2 On (VO1)
[U6] Contact Output 10 Seal-In	OFF
[U6] Contact Output 10 Events	Enabled
[U7] Contact Output 11 ID	TRIP_86Ex
[U7] Contact Output 11 Operate	BKR FAIL 2 TRIP OP
[U7] Contact Output 11 Seal-In	OFF
[U7] Contact Output 11 Events	Enabled
[U8] Contact Output 12 ID	TRIP_86JT2
[U8] Contact Output 12 Operate	TRIP 86T On (VO3)
[U8] Contact Output 12 Seal-In	OFF
[U8] Contact Output 12 Events	Enabled
[W1] Contact Output 13 ID	TRIP_52ET1_B1
[W1] Contact Output 13 Operate	TRIP 52ET2 On (VO2)
[W1] Contact Output 13 Seal-In	OFF
[W1] Contact Output 13 Events	Enabled
[W2] Contact Output 14 ID	TRIP_52ET1_B1
[W2] Contact Output 14 Operate	TRIP 52ET2 On (VO2)
[W2] Contact Output 14 Seal-In	OFF
[W2] Contact Output 14 Events	Enabled

VIRTUAL OUTPUTS

Virtual Output 1 ID	TRIP 52JT2
Virtual Output 1 Events	Enabled
Virtual Output 2 ID	TRIP 52ET2
Virtual Output 2 Events	Enabled
Virtual Output 3 ID	TRIP 86T
Virtual Output 3 Events	Enabled
Virtual Output 4 ID	START OSC
Virtual Output 4 Events	Enabled
Virtual Output 5 ID	START GENERA
Virtual Output 5 Events	Enabled
Virtual Output 9 ID	START GENERAL
Virtual Output 9 Events	Enabled
Virtual Output 11 ID	RESUMEN TRIP
Virtual Output 11 Events	Enabled
Virtual Output 13 ID	ARQ 50BF 52JT2
Virtual Output 13 Events	Enabled

SE LOS VILOS
T60 T2 S1
DEVICE DEFINITION
ORDER CODE: T60-WC3-HPH-F8L-H6D-M8L-P6E-U6C-W6C
VERSION: 8.0X
DESCRIPTION: (NONE)
INTERFACE: IP:10.1.21.49 | ADDR:254 | PORT: 502

VIRTUAL OUTPUTS (continued from last page)

Virtual Output 14 ID	ARQ 50BF 52ET2
Virtual Output 14 Events	Enabled
Virtual Output 30 ID	XFMR PCNT DIFF 2ND A
Virtual Output 30 Events	Enabled
Virtual Output 31 ID	XFMR PCNT DIFF 2ND B
Virtual Output 31 Events	Enabled
Virtual Output 32 ID	XFMR PCNT DIFF 2ND C
Virtual Output 32 Events	Enabled
Virtual Output 33 ID	XFMR PCNT DIFF 5TH A
Virtual Output 33 Events	Enabled
Virtual Output 34 ID	XFMR PCNT DIFF 5TH B
Virtual Output 34 Events	Enabled
Virtual Output 35 ID	XFMR PCNT DIFF 5TH C
Virtual Output 35 Events	Enabled
Virtual Output 127 ID	Virt Op 127
Virtual Output 127 Events	Disabled
Virtual Output 128 ID	Virt Op 128
Virtual Output 128 Events	Disabled

RESETTING

Operand	RESET LEDS On (VI1)